MODEL 5200E
RECEIVER
SCHEMATICS

tau-tron
A UNIT OF
GENERAL SIGNAL

MODEL 5200E

RECEIVER

SCHEMATICS

5200E RECEIVER SCHEMATICS
Table of Contents

B3ZS Input Schematic.....D-57743

B3ZS Input Assembly.....D-57745

Control Bit Separator Schematic.....D-57687

Control Bit Separator Assembly.....D-57689

PR Sync Schematic.....D-57328

PR Sync Assembly.....D-57330

Error Counter Schematic.....D-57150

Error Counter Assembly.....D-57152

Front Panel Display Board Schematic.....C-57376

Front Panel Display Board Assembly.....D-57233

CPU/RS232 Schematic.....D-57231

CPU/RS232 Assembly.....D-57233

STD/GPIB Interface Schematic.....D-57373

Status & Control Schematic.....D-57331

Status & Control Assembly.....D-57333

Front Panel Control Schematic.....D-57371

Front Panel Control Assembly.....D-57372

Rear Panel Interface Schematic.....D-57272

Rear Panel Interface Assembly.....C-57274

Motherboard Schematic.....E-57334

Motherboard Assembly.....D-57336

ZONE	LTR	REVISIONS DESCRIPTION	DATE	APPROVED
	I	ECN# NP 596	9/18/87	XL WCF
	A	REL PER ECO # 02945	1/7/88	PK
	B	REV PER ECO # 03027	5/4/88	JBC

NOTES:
1. CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
2. COILS IN MICRO HENRIES.
3. RESISTORS, ¼ WATT, ±5%, UNLESS NOTED.
4. □ DENOTES 470Ω ¼ W TO -5.2VDC.
5. ECL LOGIC - 5.2V TO PIN 8 GND TO PINS 1 AND 16 UNLESS NOTED
6. DATA ONLY, ADD RESISTOR TO B2 AS REQUIRED (PINS 1 TO 4 OR 2 TO 4) IN TEST.
7. VCO, CHANGE VALUE OF C3 AS REQUIRED IN TEST.

FUNCTION	CONTROL LINE B	A
DSX	0	0
HI-LO	0	1
BIN D+C	1	0
BIN D ONLY	1	1

REFERENCE DESIGNATIONS LAST USED	UNUSED	
R196	R38	C17
C100	R42	C33-35
CR12	R76	C75
Q11	R89	C80
L22	R110	L6
	R112	L7
	R113	
	R160	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

D-57745	S5200D
NEXT ASSY	USED ON

APPLICATION

CONTR NO.

DR G.J. Ipsita	5/8/87	
CHK R.K.	5/13/87	
APPD WCF Letts	5/13/87	

APPROVED

BY DIRECTION OF

tau-tron inc

DRAWING TITLE
B3ZS INPUT SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	4	57743	B

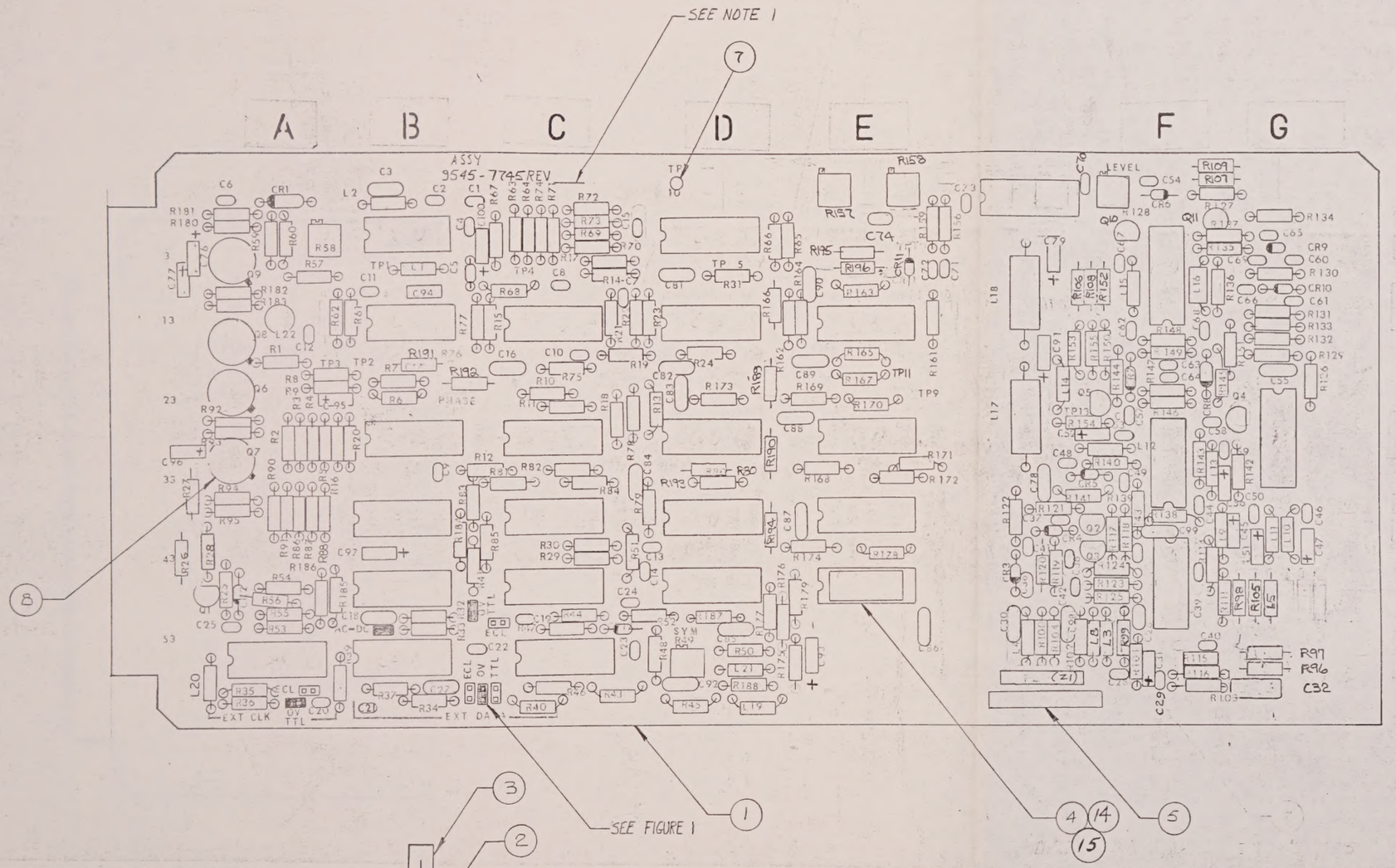
SCALE | S5200D | SHEET 1 of 1

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

NOTICE OF CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL IS THE PROPERTY OF TAU-TRON INC. IS TO BE USED SOLELY FOR THE PURPOSE PROVIDED AND IS NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU-TRON.

REVISIONS				
		DESCRIPTION	DATE	APPROVED
	1	REV PER NPS96 ECN 9-13-87	9/23/87	RL WGL
	A	REL PER 02945 MCO 11/6/88	11/7/88	PDR



PLACE JUMPERS OVER PINS 4 PLACES AS SHOWN BY ■

FIGURE 1

NOTES:

1. PLACE THE LATEST ASSEMBLY PARTS LIST REVISION MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. INSTALL SCREWS FROM TOP SIDE.

D-57626	55250
E-57158	55200E RX
D-57350	55200D RX
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL

DR	R.J. Gaston	5/8/87
CHK	R.L.	5/13/87
A	W.E. Little	5/13/87
APPROVED		
M DIRECTOR		

tau-tron

DRAWING TITLE
B3ZS INPUT PCB ASSEMBLY

SIZE D | CODE IDENT NO. 55 | DRAWING NO. 57745 | REV A

SCALE 2/1 | 1 OF 2

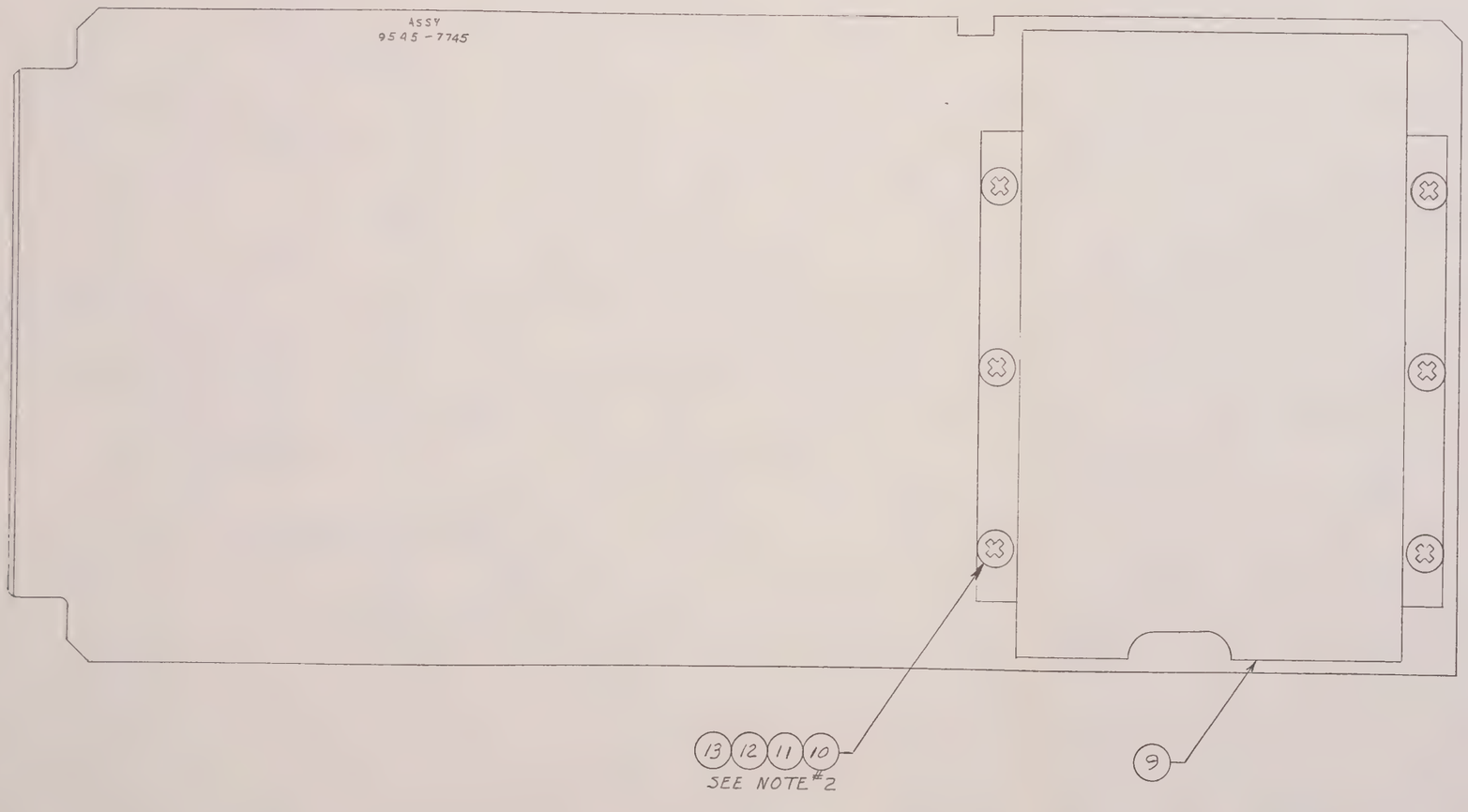
NOTICE OF
CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL, IS THE PROPERTY OF TAU TRON INC., IS TO BE USED SOLELY FOR THE PURPOSE PROVIDED, AND IS NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU TRON INC.

8 7 6 5 4 3 2 1

			APPROVED
	SEE SHEET 1		

D C B A



WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±
SEE SH. #1	MATERIAL:
NEXT ASSY / USED ON	
APPLICATION	

CONTR NO.	
DR	
CHK	
APPD	
APPROVED	
BY DIRECTION OF	

tau-tron
A UNIT OF GENERAL SIGNAL

DRAWING TITLE
B3ZS INPUT
PCB ASSEMBLY

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57745	A

57745

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	REL. PER ECO # 01968	9-14-84	9-17-84
	B	UPDATED PER DOC ACCURACY PROG PER ECO # 02178 7-9-85 BN	7/24/85	PK

NOTES:
1. UNLESS OTHERWISE SPECIFIED
   A. RESISTANCE VALUES ARE IN OHMS, ±5%, 1/8 W.
   B. CAPACITANCE VALUES IN MICROFARADS.
2. ⊖ DENOTES 470 OHM RESISTOR (SIP PKG) TO -5.2V
3. □ DENOTES EDGE CONNECTOR PIN NO. OR LETTER.
4. SEE SHEET 2 FOR LAST USED REFERENCE DESIGNATIONS.

SHT 2 U11/8 M FRAME (L)
SHT 2 U12/8 F FRAME (H)
D.S. CLOCK 47
SHT 2 U6/14 SLIP (L)
DATA 37
SLIP REQUEST
M-BIT INCORRECT
SHT 2 CR9-C V REF
GAPPED CLOCK 48
LOSS OF SIGNAL (L) 33
FRAME ERROR 35
FRAME ERROR 32
PARITY ERROR 36
PARITY ERROR 38
X-BIT 24
SHT 2 U27/4
SHT 2 U32/6 & 8
SHT 2 U 2/2
SHT 2 U 2/4
SHT 2 U28-7 M SYNC
F ENABLE (L)
M1 ENABLE
M2 ENABLE
M ENABLE (L)
P2 ENABLE (L)
X ENABLE (L)
1 = CORRECT
1 = INCORRECT
EVEN = 1 ODD = 0
ONES COUNT (L)
PARITY
CLOCK-1
GAPPED CLOCK-1 44.2097 MHZ
1.9 μs
300 NS
M-BIT CORRECT
F-BIT CORRECT
F-BIT INCORRECT
GND TO INJECT V BIT ERROR
GND TO INJECT F BIT ERROR

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±
NEXT ASSY: 57650	MATERIAL:
USED ON: APPLICATION	

CONTR NO.		
DR D. OLIVER	26DEC79	
CHK R.O. Tanner	26 Dec 79	
APPD W.E. Smith	27 Dec 79	
Y. Kuhl	4 Jan 80	
P. Tanner	9/24/81	
APPROVED		
BY DIRECTION OF		

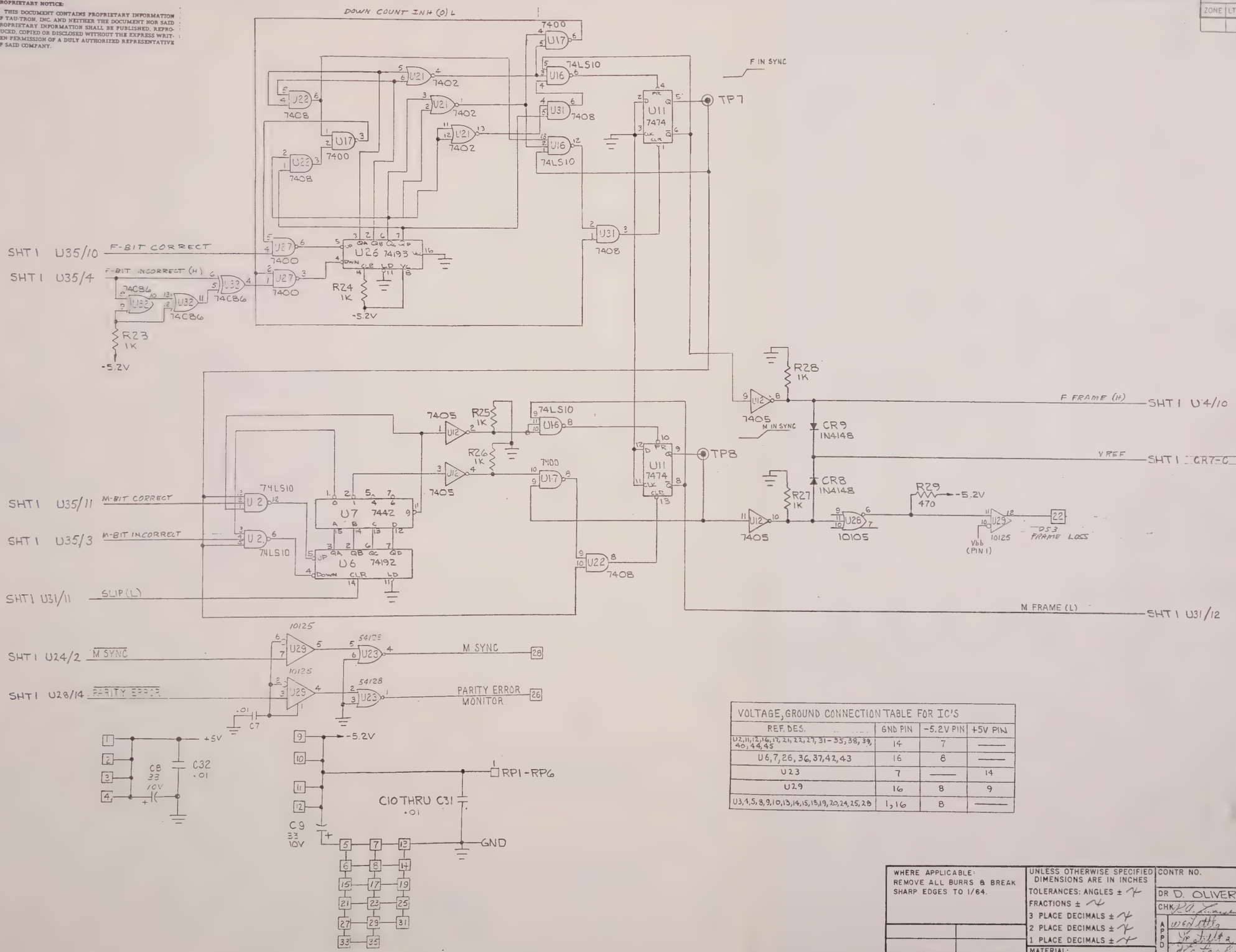
tau-tron INC Chelmsford, Mo. 01824

DRAWING TITLE
CONTROL BIT SEPARATOR SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57687	B
SCALE NONE	5250	SHEET 1 OF 2	

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		



VOLTAGE, GROUND CONNECTION TABLE FOR IC'S			
REF. DES.	GND PIN	-5.2V PIN	+5V PIN
U2, 11, 12, 16, 17, 21, 22, 27, 31-35, 38, 39, 40, 44, 45	14	7	——
U6, 7, 26, 36, 37, 42, 43	16	8	——
U23	7	——	14
U29	16	8	9
U3, 4, 5, 8, 9, 10, 13, 14, 15, 18, 19, 20, 24, 25, 28	1, 16	8	——

LAST REF DES USED	UNUSED
U45	U1, 30, 41
C34	
R41	
Q4	
CR10	CR5
TP9	
RP6	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

NEXT ASSY	USED ON
	5250

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL: ~

CONTR NO.

DR	D. OLIVER	26DEC79
CHK	R.D. Fraser	26 Dec 79
APPD	W.E. Little	27DEC79
APPD	Sr. Staff Engr	4/14/81
APPD	Mr. Taylor	5/12/81

APPROVED /
BY DIRECTION OF Sr. Staff Engr

tau-tron inc Chelmsford, Mass. 01824

DRAWING TITLE
CONTROL BIT SEPARATOR SCHEMATIC

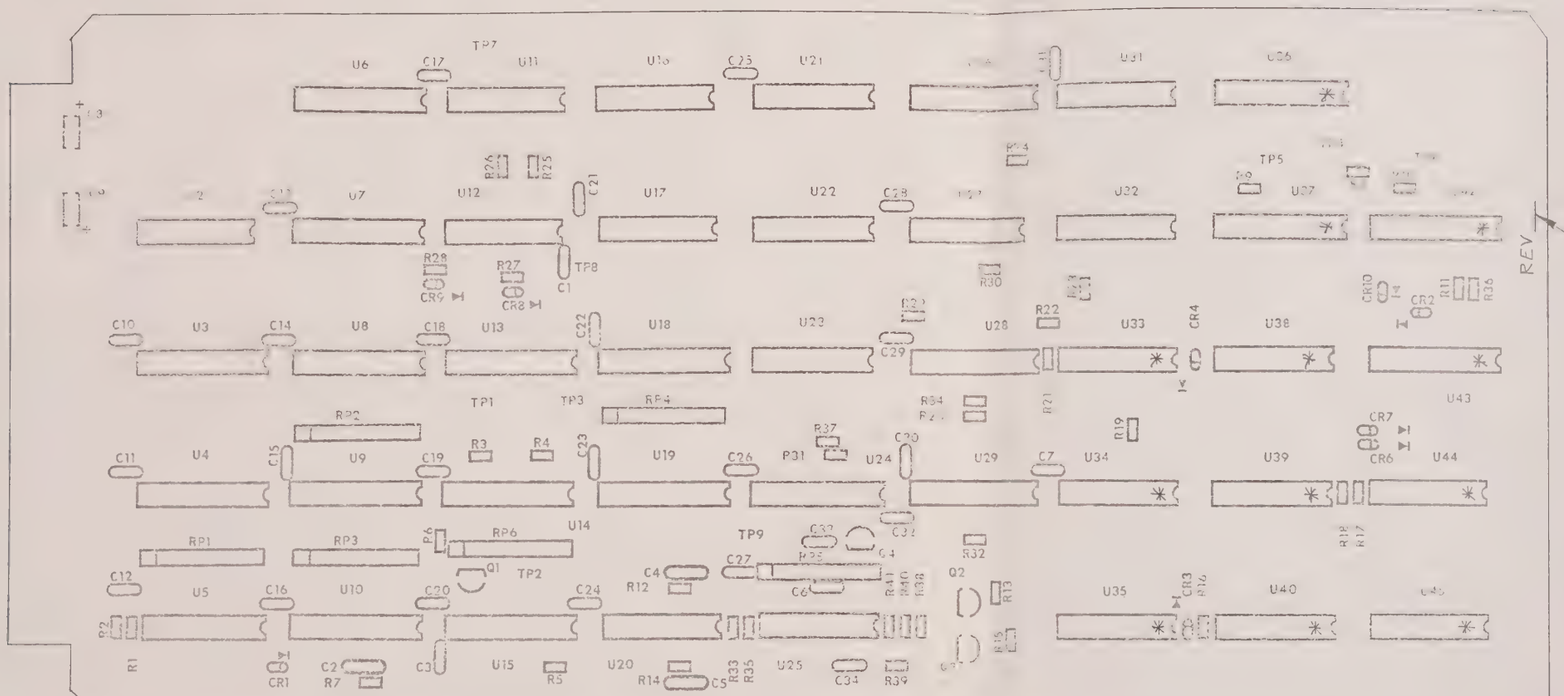
SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	7	57687	B

SCALE ~ | 5250 | SHEET 2 OF 2

NOTICE OF CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL (EITHER PURSUANT TO LAW OR TO CONTRACT) AND IS TO BE USED SOLELY FOR THE PURPOSE(S) FOR WHICH IT IS FURNISHED AND NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU-TRON.

REV	DESCRIPTION	DATE	APPROVED
B	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO #02178 7-1-85 DH		



NOTES:

1. * DENOTES A STATIC SENSITIVE DEVICE.
2. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

NEXT ASSY	USED ON
57690	5260
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ± 1°
FRACTIONS ± 1/64
3 PLACE DECIMALS ± .010
2 PLACE DECIMALS ± .02
1 PLACE DECIMALS ± .1
MATERIAL:

BMB	5-23-84
JE	6-1-84
A.C	9/19/84
	9/20/84
SAH	9/7/89

tau-tron
A UNIT OF GENERAL RESIDUAL

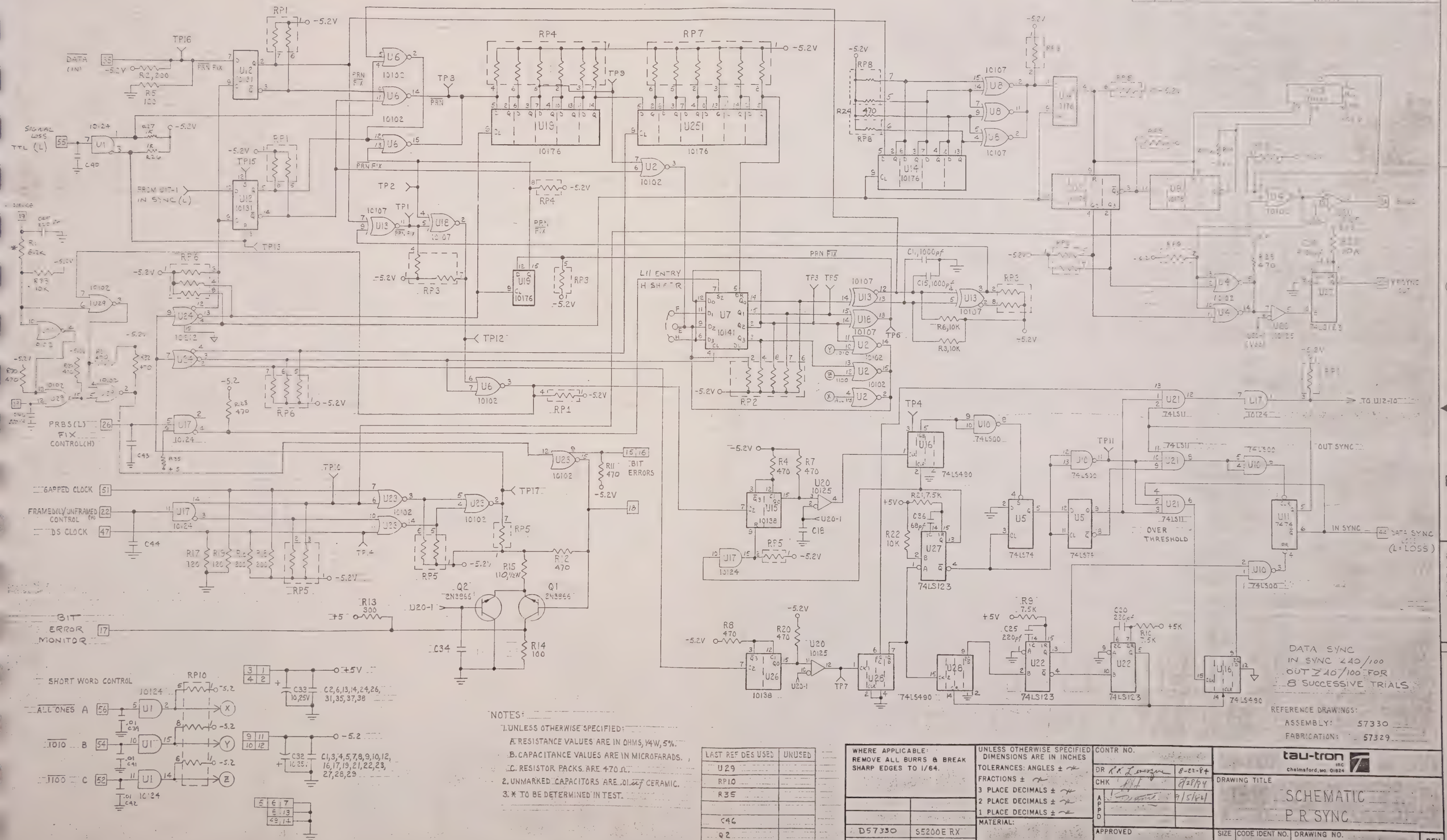
DRAWING TITLE
ASSEMBLY
CONTROL BIT SEPARATOR

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	954	57689	B

SCALE 2/1 | 5250 | SHEET 1 OF 1

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	PER ECO# 02135 - REMOVE 10K R3	5/6/85	PK
	B	REV PER ECO# 02882 PCO 7/10/87	9/11/87	PK
	C	REV PER ECO# 02930 PCO 11/8/88		



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, ¼W, 5%.
   B. CAPACITANCE VALUES ARE IN MICROFARADS.
   C. RESISTOR PACKS ARE 470 Ω.
2. UNMARKED CAPACITORS ARE .01 mf CERAMIC.
3. * TO BE DETERMINED IN TEST.

REFERENCE DRAWINGS:
ASSEMBLY: 57330
FABRICATION: 57329

LAST REF DES USED	UNUSED
U29	
RP10	
R35	
C46	
Q2	
TP17	

D57330	55200E RX
NEXT ASSY	USED ON

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.
DR K.K. Lonergan 8-22-84
CHK 8/28/84
APPD 9/5/84
APPROVED
BY DIRECTION OF

tau-tron inc Chelmsford, ma. 01824

DRAWING TITLE
SCHEMATIC
P.R. SYNC

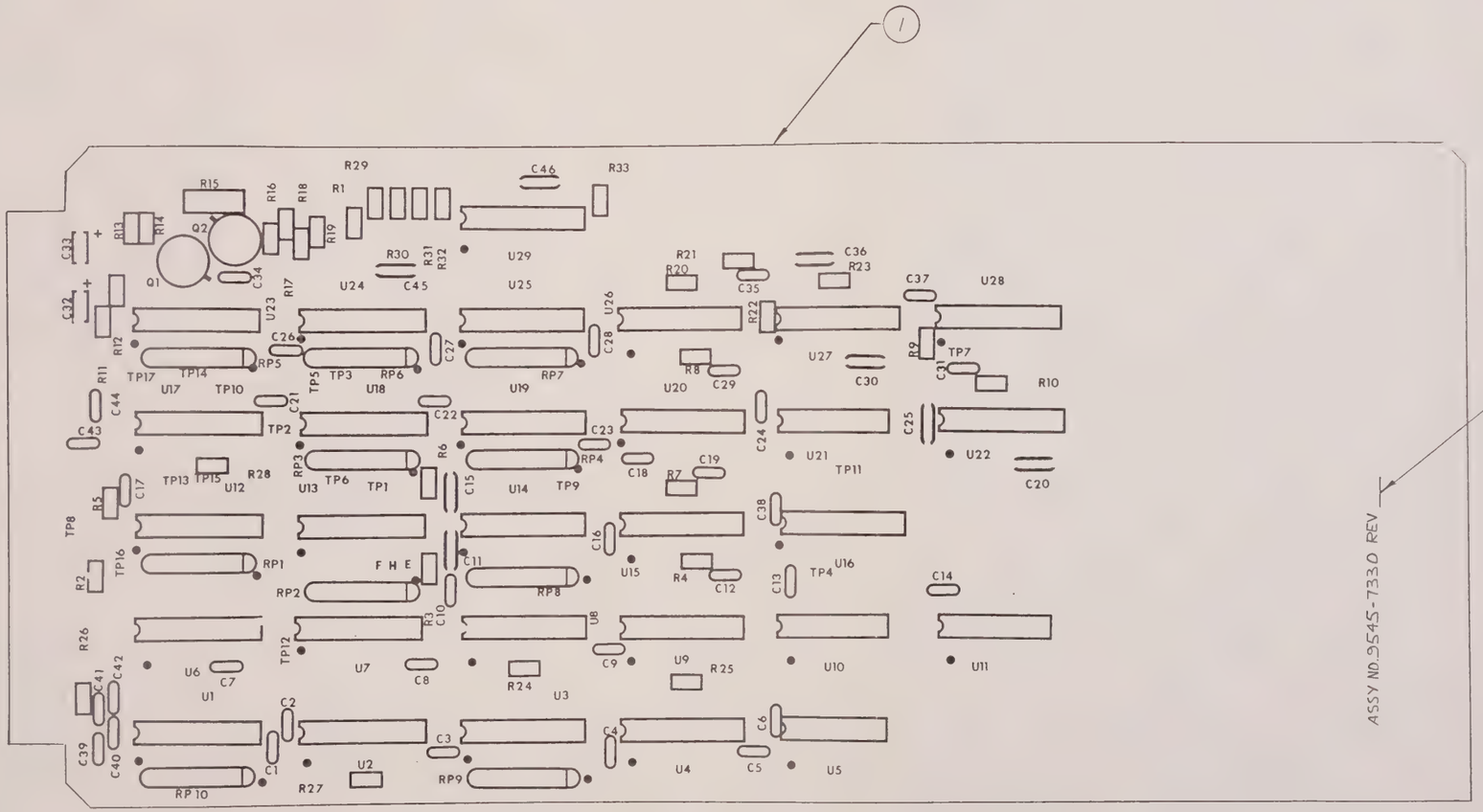
SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57328	C

SCALE: NONE SHEET 1 OF 1

NOTICE OF CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL IS THE PROPERTY OF TAU-TRON INC IS TO BE USED SOLELY FOR THE PURPOSE PROVIDED AND IS NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU-TRON INC

	B	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO# 02233 8-23-85 @ H	8/30/85	PK



SCREEN
57329 A

NOTES:
1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64		CONTR NO	tau-tron (A UNIT OF GENERAL SIGNAL)
		DR T.M. 8-30-84	DRAWING TITLE: ASSEMBLY PR SYNC PC BOARD
	SS200E RX	CHK R.S.L 9-10-84	
NEXT ASSY	USED ON	APPD J.E. 9-10-84	
APPLICATION		APPROVED	SIZE D · CODE IDENT NO 954 · DRAWING NO. 57330 · REV B
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES. TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ± MATERIAL.		BY DIRECTION OF	

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR
	D	REV PER ECO 01051 11-19-81 RF	12-29-81	EL
	E	REV PER ECO-01107 R.D. January 3-4-82	3-4-82	
	F	UPDATED PER DOC ACCURACY PROG PER ECO# D2194 8-5-85 DH	7/23/85	DS

ZONE	LTR		DATE	APPR
	1	UPDATED C.S. 9-25		
	A	REV PER DCN00789 C.A. 1-16-81		
	B	[illegible]		
	C	REV PER ECO 81011 [illegible]		

DO BUS
R9 1K
U12 74LS32
U11 74LS08
U21 74LS04
U10 74LS125
R12 1K
R11 1K
R7 510
R8 510
+5
00 ADDRESS
BUS ENABLE (LI13-19)
R10 4.7K
+5V
U9 P309
U0 74LS04
U7 74154
A0 (U22-9)
A1 (U18-1)
A2 (U15-1)
A3 (U16-1)
A4
A5 (U19-1)
A6 (U18-1)
A7 (U17-1)
A8 (U33-1)
A9 (U20-1)
(U13-1)
U52 74S74
U53 74123
C2 150
R17 3.6K
(L6-8)
U54 74LS74
U32 74LS11
U39 74LS04
C22 150
R18 100
C1 10µ25V
C2-4,7-16 .01
-5.2V
C5 .01

CLEAR
U24 74LS393
U23 74LS393
U22 74LS393
U12 74LS374
U15 74LS374
(SH.2, U38-13)
A3
A2
DO BUS
D1 BUS
D2 BUS
D3 BUS
D4 BUS
D5 BUS
D6 BUS
D7 BUS
U13 74LS245
(U10-5)
ENABLE (U9-11)
BIT ERRORS (SH.2, U43-11)
U9 74S00
BIT ERRORS
U26 74LS393
U25 74LS393
U17 74LS374
U18 74LS374
A7
A6
A5
PAR LATCH STROBE (U48-6)
U27 74LS374
A9
PAR ERRORS
U4 74S00
PAR GRANT (U39-1) SHEET-2
R6 100
STOP/START
U33 74LS125
PES FLAG
A10
B.E.S. FLAG
(U7-14) AC
P.E.S. START
B.E.S. START
U34 74S74

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
a) RESISTANCE VALUES ARE IN OHMS, ¼W, ±5%
b) CAPACITANCE VALUES ARE IN MICROFARADS

LAST REF. DES. USED				
U53	R21	C24		

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	
D57152	53200D RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ½
FRACTIONS ± ½
3 PLACE DECIMALS ± ½
2 PLACE DECIMALS ± ½
1 PLACE DECIMALS ± ½
MATERIAL: ½

CONTR NO.
DR K. TRAMPLE 1/13/80
CHK BUDD / [illegible] 7-31-80
APPD K. Krueger 10/17/80
APPROVED
BY DIRECTION OF

tau-tron inc
Chelmsford, MA. 01824

DRAWING TITLE
53200D ERROR COUNTER PC. BD
(TOTALIZE) (ADDRESS DECODE)
SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	½	57150	F

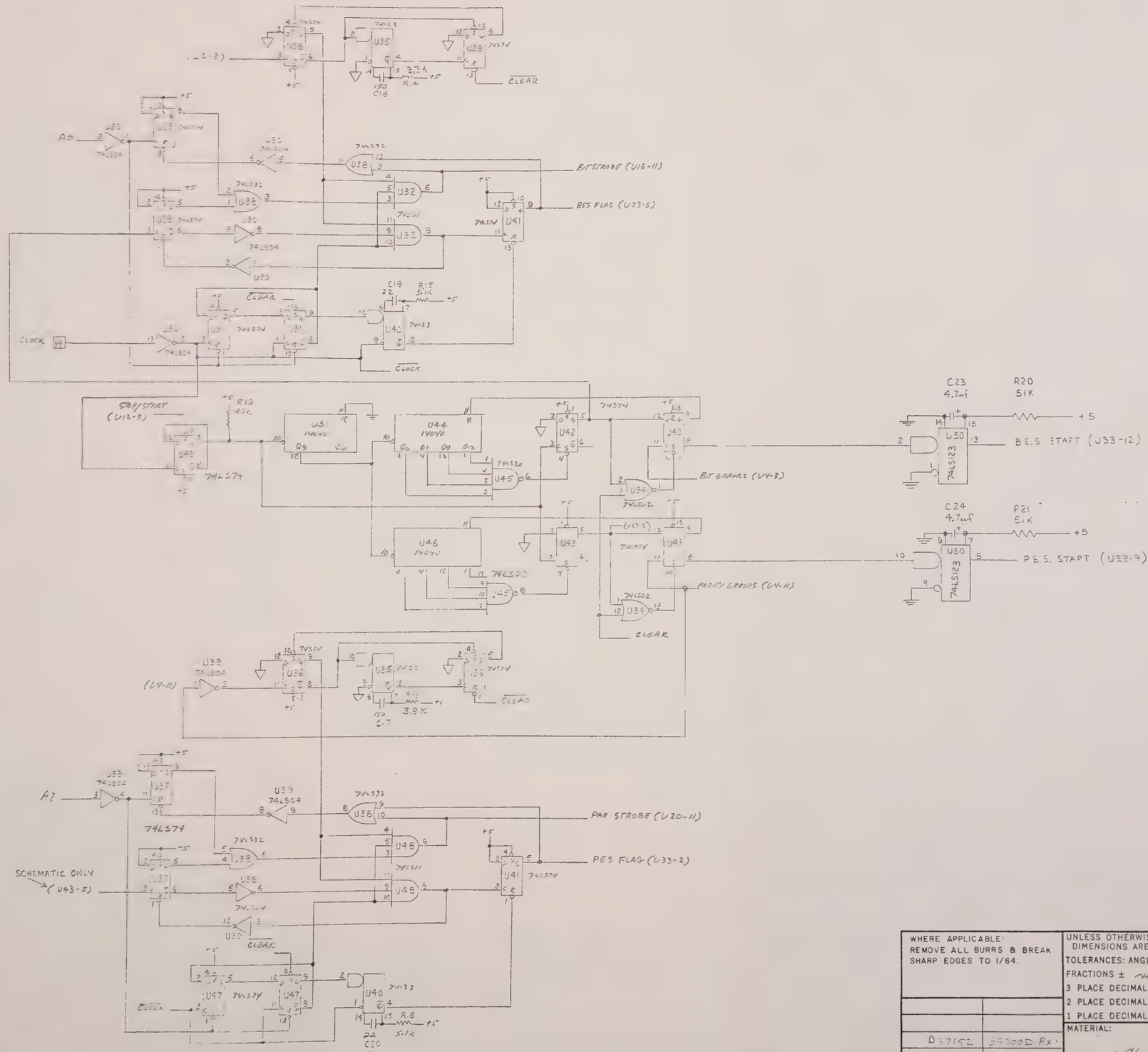
SCALE ½ | 53200D RX | SHEET 1 OF 2

57150

PROPRIETARY NOTICE:

THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

LTR	TO	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		



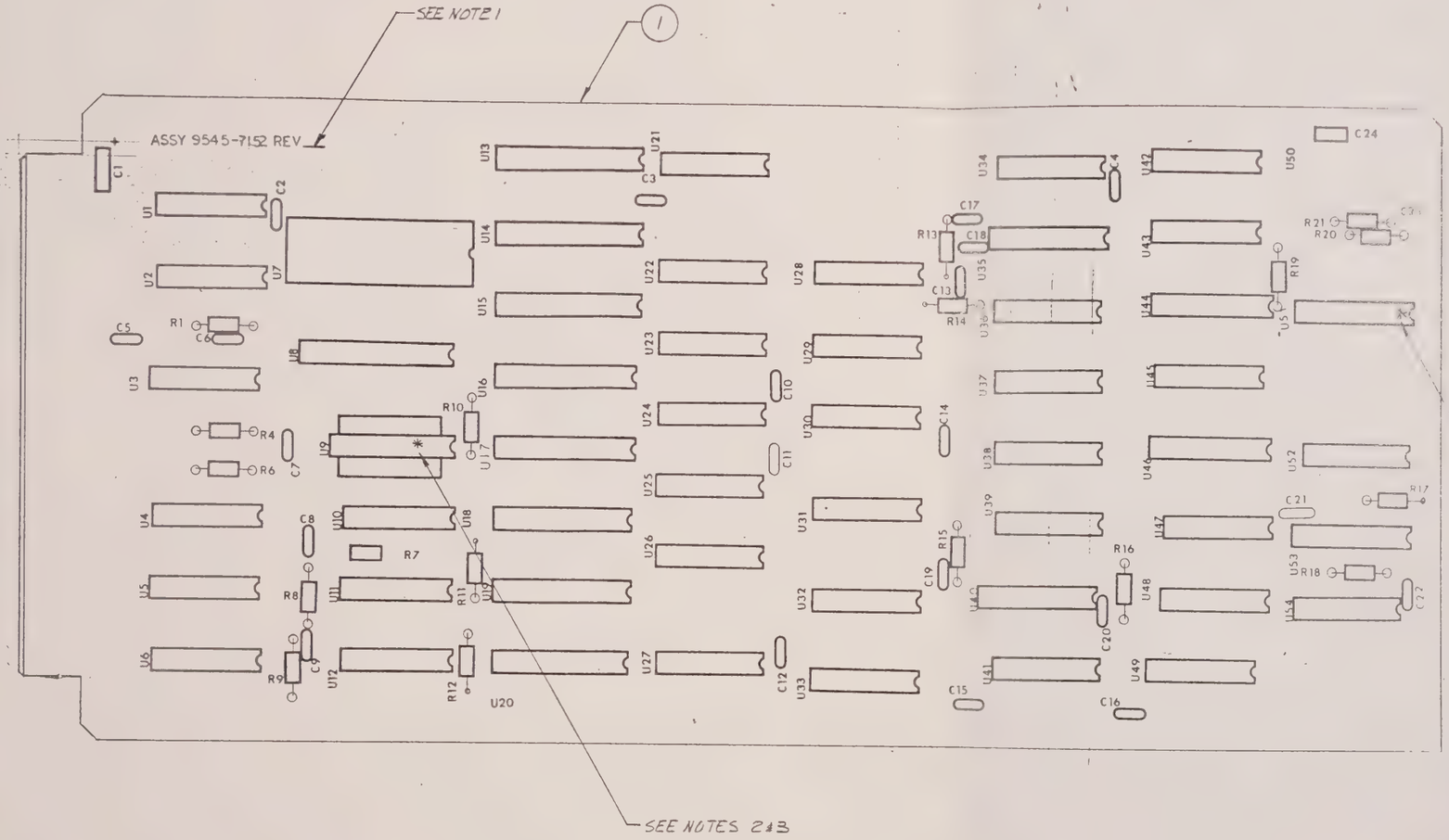
WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± 1° FRACTIONS ± 1/64 3 PLACE DECIMALS ± .010 2 PLACE DECIMALS ± .01 1 PLACE DECIMALS ± .1 MATERIAL:	CONTR NO.	tau-tron INC Chelmsford, Mo. 01824
		DR K. Beaupre 8/24/82	DRAWING TITLE: SS200D ERROR COUNTER PC. BD. SCHEMATIC
		CHK E. HC	
D37152 / 3300D Rx		APPD	
NEXT ASSY / USED ON		APPROVED	SIZE D · CODE IDENT NO. 74 · DRAWING NO. 57150 · REV F
APPLICATION		BY DIRECTION OF	SCALE 1/1 · 3300D Rx · SHEET 2 OF 2

57150

NOTICE OF CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL IS THE PROPERTY OF TAU-TRON INC IS TO BE USED SOLELY FOR THE PURPOSE PROVIDED AND IS NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU-TRON INC

J	UPDATED & REDRAWN PER DOC ACCURACY PROG^ PER ECO# 02194 8-13-85 DH		



NOTES:
1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. *DENOTES STATIC SENSITIVE DEVICE.
3. TO BE INSTALLED BY TEST.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES "CLEARANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±
D57152 / 55200D RX	MATERIAL
NEXT ASSY / USED ON	
APPLICATION	

CONTR NO		
DR C.A	12-28-81	
CHK B.C.	2-3-81	
APPD K.B.	2-3-81	
APPROVED		
BY DIRECTION OF		

tau-tron A UNIT OF GENERAL SIGNAL

DRAWING TITLE
ASSEMBLY
ERROR COUNTER PCB

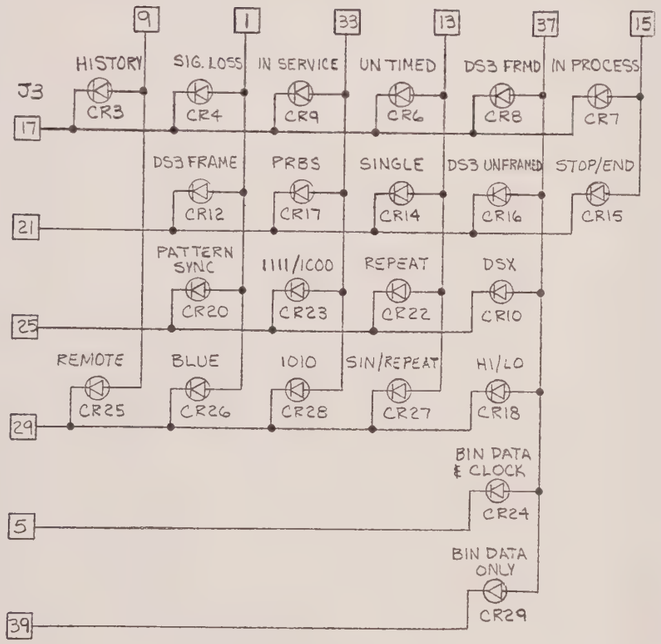
SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	954	57152	J

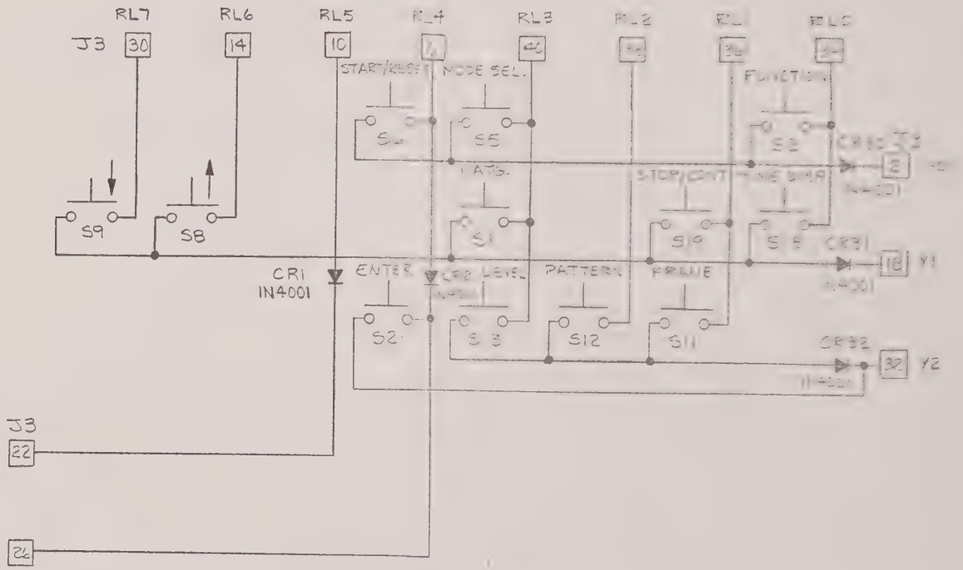
SCALE 2:1 | 55200D RX | SHEET 1 OF 1

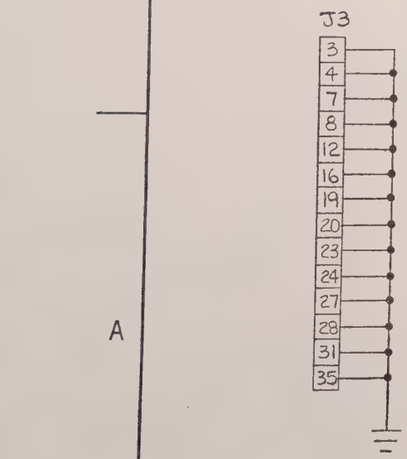
NOTICE OF CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL, IS THE PROPERTY OF TAU-TRON INC., IS TO BE USED SOLELY FOR THE PURPOSE PROVIDED, AND IS NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU-TRON INC.

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	REV PER ECO#02125 TNM 4-4-85	5/6/85	







LAST REF. DES. USED		
USED	UNUSED	
S21	CR5	S14
CR32	CR11	S15
	CR13	S16
	CR19	S17
	CR21	S20
	S3	
	S4	
	S7	
	S10	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

57349	55200E RX
NEXT ASSY	USED ON

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± AK
FRACTIONS ± AK
3 PLACE DECIMALS ± AK
2 PLACE DECIMALS ± AK
1 PLACE DECIMALS ± AK
MATERIAL: AK

CONTR NO.

DR	T. MURPHY	4-4-85
CHK	[illegible]	85APR8
APPD		

APPROVED

BY DIRECTION OF

tau-tron A UNIT OF GENERAL SIGNAL

DRAWING TITLE: SCHEMATIC FRONT PANEL DISPLAY BD.

SIZE	CODE IDENT NO.	DRAWING NO.	REV
C		57376	A

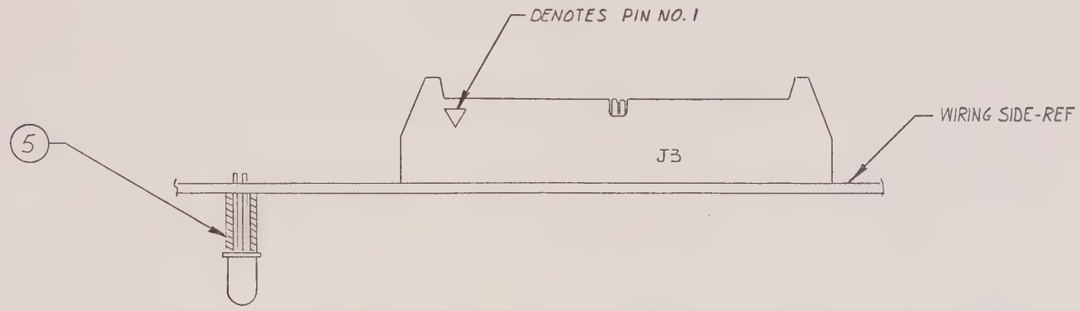
SCALE AK | 55200E | SHEET 1 OF 1

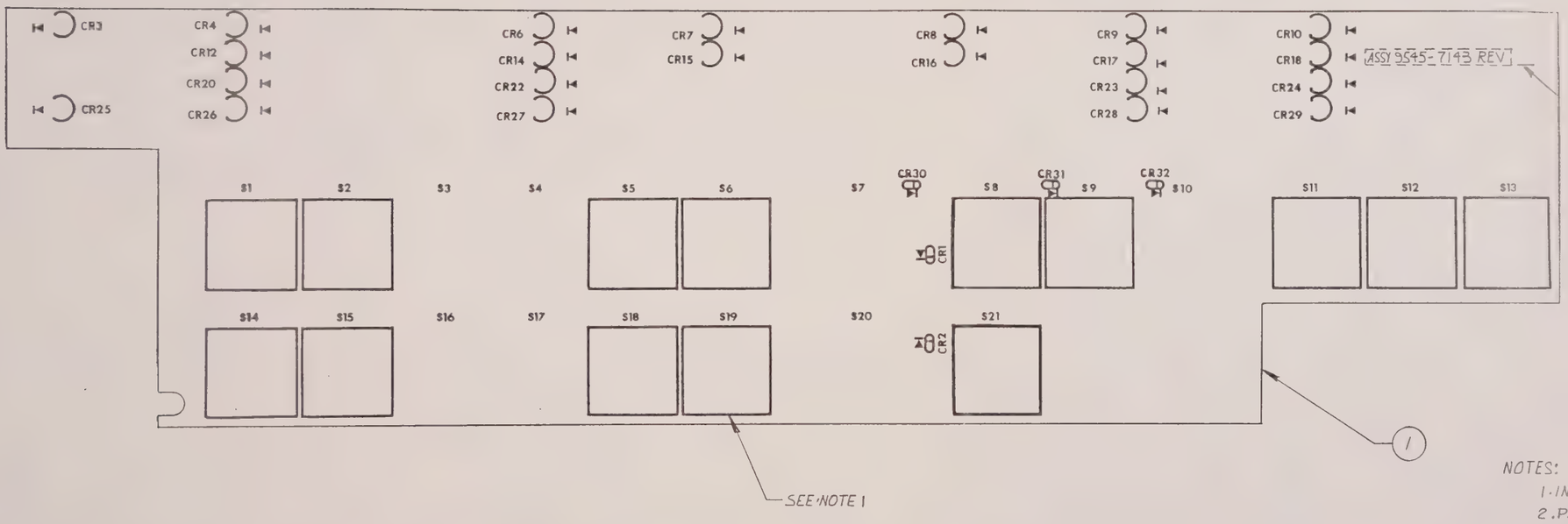
57376

Charrette N57754

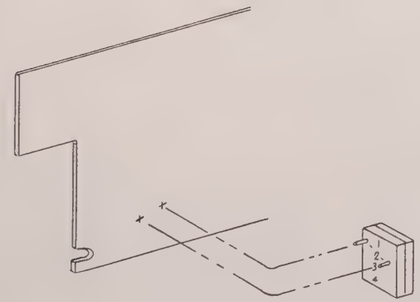
NOTICE OF
CONFIDENTIAL INFORMATION
INFORMATION CONTAINED HEREIN
IS CONFIDENTIAL IS THE PROPERTY
OF TAU-TRON INC IS TO BE USED
SOLELY FOR THE PURPOSE FOR
WHICH AND SHALL NOT BE DISCLOSED
TO OTHERS WITHOUT THE PRIOR
WRITTEN CONSENT OF TAU-TRON
INC

	B	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO 02233 8-26-85 BH	8/30/85	PK





NOTES:
1. INSTALL SWITCHES AFTER WAVE SOLDERING. CLEAN W/ ISOPROPYL ALCOHOL.
2. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
3. CHANGE ASSY NO. TO 9545-7349.



INSTALL ALL SWITCHES, WITH NUMBERS STAMPED ON REAR, AS SHOWN

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±
DS7351 / S5200E RX NEXT ASSY / USED ON	MATERIAL

CONTR NO		
DR MEH	11-20-84	
CHK		
APPD		
APPROVED		

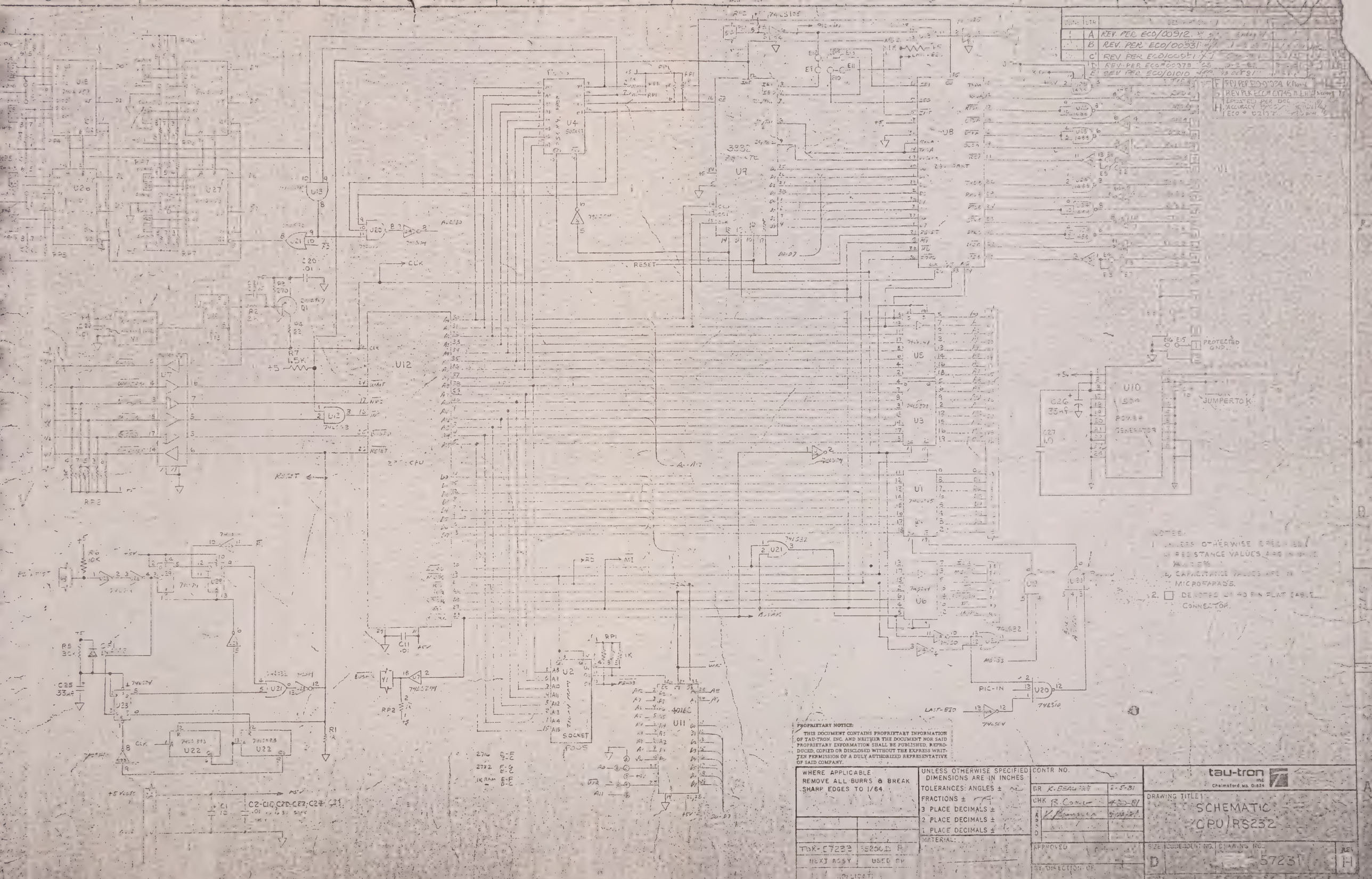
tau-tron
A UNIT OF GENERAL SIGNAL

DRAWING TITLE
FRONT PANEL DISPLAY BOARD ASSEMBLY

SIZE	CODE IDENT NO	DRAWING NO	REV
D	954	57349	B

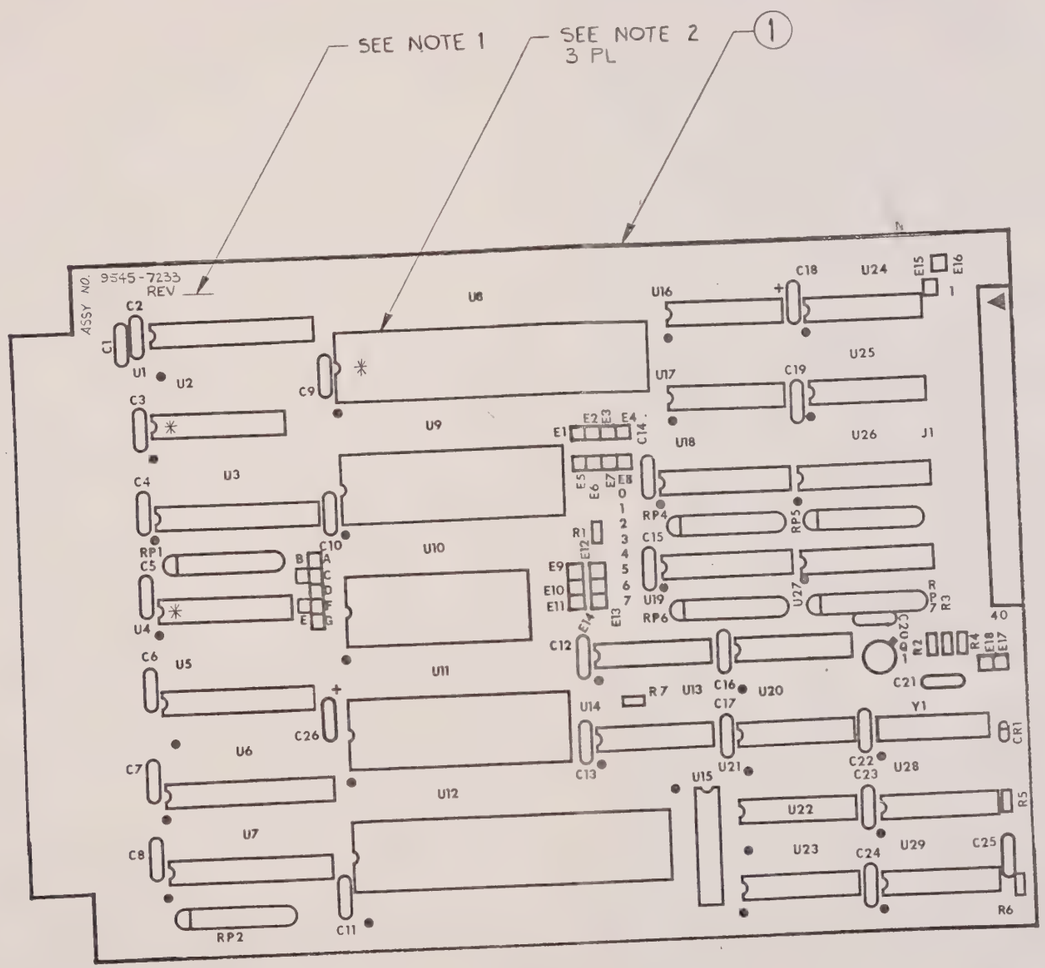
57349

LTR	
A	REV. PER ECO/00912.
B	REV. PER ECO/00931.
C	REV PER ECO/00971.
D	REV. PER ECO#00978.
E	REV PER ECO/01010.
F	REV PER ECO#01028.
G	REV PER ECO#01145.
H	UPDATED PER DCR ACCURACY ECO# 02197.



ARY NOTICE:
OCUMENT CONTAINS PROPRIETARY INFORMATION
RON, INC. AND NEITHER THE DOCUMENT NOR SAID
TARY INFORMATION SHALL BE PUBLISHED, REPRO-
DUCED OR DISCLOSED WITHOUT THE EXPRESS WRIT-
MISSION OF A DULY AUTHORIZED REPRESENTATIVE
OMPANY.

	H	REDRAWN, WITH CHANGES PER DOC ACCURACY PROG ECO # 02197		
	J	SHT. 2 NOW OBS. REV PER		
	K	REV PER ECO # 02722		



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSY IN THE DESIGNATED AREA.
2. * DENOTES STATIC SENSITIVE DEVICE.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	DRAWN	tau-tron
		DFC ATHERTON 4-9-81	CPU/RS32 ASSY
		CHK B COOPER 4-22-81	
		APP K. BEANPRE 4-22-81	
E-57158 S5200D RX	MATERIAL		D 57233 K
NEXT ASSY USED ON			2/1 S5200D RX 1 OF 1

PROPRIETARY NOTICE:

THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	REV PER ECO #02125	5/6/85	PK

STD BUS

A0-A15

D0-D7

U8 U4 U14 U11 U18 U9 U3 U6 U7 U16 U20 U2 U5 U19 U15 U17 U10 U12 U13

J1

RP1-RP9

R1 R2 R3

CR1

(S) SRQ

430Ω

REMOTE LISTEN TALK

NOTES:
1. ⏚ DENOTES 1.5K TO +5V (VCC).
2. HIGHEST REF DES USED: C17, CR4, J1, R8, RP4, U20

NEXT ASSY	57269
USED ON	S5200ERX
APPLICATION	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.
DRN M. Spindler 85MAR28
CHK
APPD K. Estella 3/29/85
APPROVED
BY DIRECTION OF

tau-tron INC. Chelmsford, Mo. 01824

DRAWING TITLE
SCHEMATIC
STD/GPIB INTERFACE

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	1	57373	A

SCALE NONE | S5200E | SHEET 1 OF 1

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE DISCLOSED OR USED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	REV PER ECO# 02125	5/16/85	PK
	B	UPDATED PER DOC ACCURACY PROG PER ECO# 02233 8-27-85		

+5 15μf C1

RP1 6.8K

CR1 1N4001 (3) R7 10Ω CR3 CR2 R8 47Ω 1.2V 1.2V 1.2V

C11 33μf EAT J2 OFF ON U2 BATSW

(1.2V AMP-HR CAP.)

POWER DOWN DRAIN
230 mh TYP.
1030 mh MAX.

74LS32 U25 MEMRQ RD WR U23 MRD MWR

74LS02 U19 IORQ MEMRQ R13 1.5K

74LS04 U21

74LS30 U20

74LS138 U26 IORQ 80 81 83 87

74LS138 U27 A0 A1 A2 88 89

(2K x 8) 69088 U13 E PROM E2, A10 E14 E11, +5V E13, A11 E15 E11, +5V E16, MWR

U7 555 R2 5.1K R1 5K C5 .01 C6 .01

U34 74LS02

C3 .01 U1

U8 LM 386N-4 C10 C9 10 mf R3 10Ω C12 .01 J2

+5V .01 C2, 4, 7, 8, 14 THRU 28 GND

OUTPUTS

J2 IN SERVICE PATTERN FRAME CTRL B CTRL A MB-19

U24

RP2 1.5K S1 (OPTION SWITCHES) ON

74LS244 U23

74LS10 U40 74LS08 U34 82 RD

74LS32 U25 84

RP2 1.5K

INPUTS J2 A B C MB-39

J1 R. PANEL SHORT PAT SWITCH

U3 74LS244 U4

69083 U5 (1K x 4) 6514 NON VOL RAM U9

69082 U11 (1K x 4) 6514 NON VOL RAM U10

U12 74LS245

U16 69091 E21, A10 E26 E26, +5V

U15 69090 E3, A10 E24 E22, +5V

U14 69089 E21, A10 E18 E17, +5V

E1, A11 E26, +5V E27, MWR

E4, A11 E22, +5V E23, MWR

E12, A11 E17, +5V E20, MWR

U1 74LS74 CCR

BATSW R6 330 C13 1μf CR5 HPS082-2800 CR4 HPS082-2800 R4 1.5K R5 510Ω Q1 2N 2784

NON VOL RAM ENABLE

REF DESIGNATIONS	
LAST USED	UNUSED
C32	U22
CR5	
J2	
Q1	
R16	
RP5	
U46	
Y1	

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. RESISTANCE VALUES ARE IN OHMS, 1/4W ±5%
B. CAPACITANCE VALUES ARE IN MICROFARADS.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64

D-57333	55200E 2X
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.

DR	BUDD	23 AUG 84
CHK	PC	8 29 84
APPD	L Sonn	1/15/84
APPROVED		
BY DIRECTION OF		

tau-tron inc Chelmsford, Ma. 01824

DRAWING TITLE: SCHEMATIC STATUS & CONTROL BOARD 55200E RECIEVER

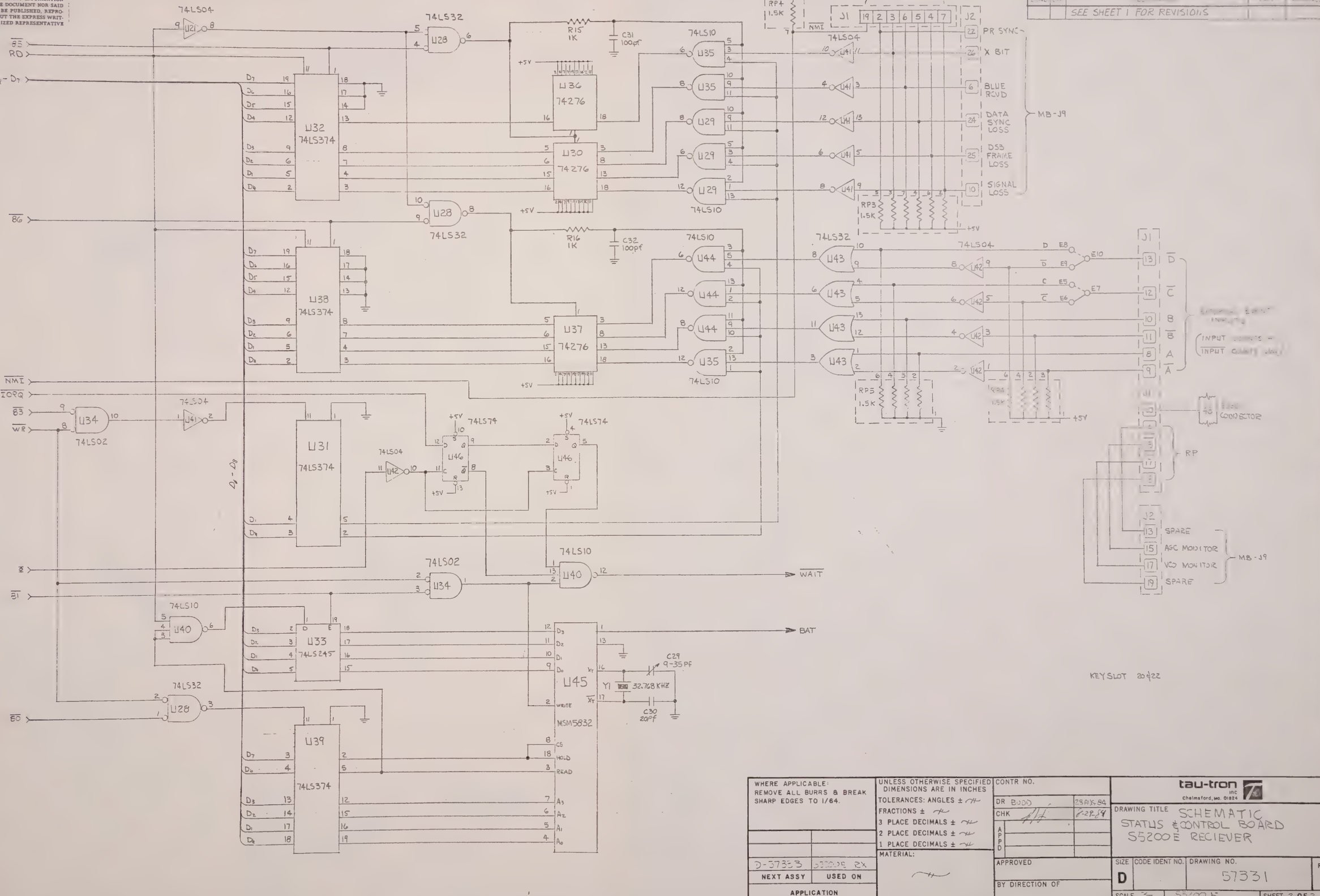
SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57331	B

SCALE | 55200E | SHEET 1 OF 2

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

8	7	6	5	4	3	2	1

LTR	ECN	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		

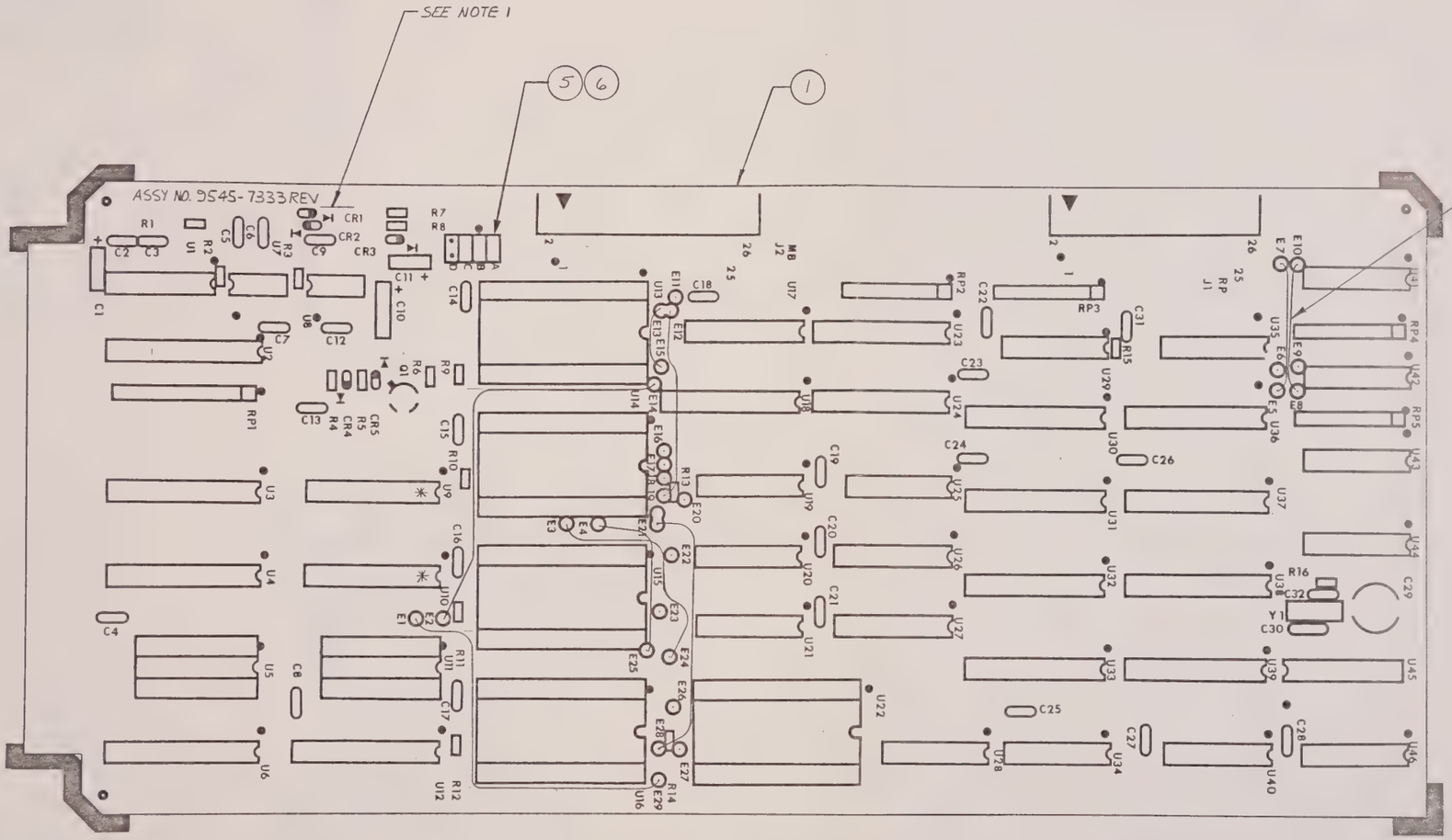


WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ± MATERIAL:	CONTR NO.	tau-tron inc Chelmsford, Mo. 01824
D-57353 / S5200E RX NEXT ASSY / USED ON APPLICATION		DR BUDD 23AUG84 CHK 8-28-84 APPD APPROVED BY DIRECTION OF	DRAWING TITLE SCHEMATIC STATUS & CONTROL BOARD S5200E RECIEVER SIZE D / CODE IDENT NO. / DRAWING NO. 57331 / REV B SCALE / S5200E / SHEET 2 OF 3

NOTICE OF CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL IS THE PROPERTY OF TAU-TRON INC IS TO BE USED SOLELY FOR THE PURPOSE PROVIDED AND IS NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU-TRON INC

NOTE	REV	DESCRIPTION	DATE	APPROVED
	B	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO# 02233 8-23-85 DH		
	C	REV PER ECO# 02267 TMM 10/15/85	11/15	
	D	REV PER ECO# 02352 MCD 7/3/86	7/11/86	
	E	REV PER ECO # 02579 MCD 8/8/86	8/12/86	
	E1	REV PER AUDIT # A0768 KRC 4-5-88		



57332 A
COMPONENT SIDE SCREEN

NOTES:

1. PLACE THE PARTS LIST REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. * DENOTES A STATIC SENSITIVE DEVICE.
3. JUMPERS 1 INCH OR LESS TO BE #24 AWG BUS WIRE WITH SLEEVING. ALL OTHERS TO BE #26 AWG SOLID WIRE.
4. U5, 11 & 13 THRU 16 TO BE INSTALLED IN TEST.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	
	S5200E RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL

CONTR NO		
DR T.M		9-10-84
CHK R.S.L.		9-12-84
APPD	J.E.	9-12-84
APPROVED		
BY DIRECTION OF		

tau-tron
A UNIT OF GENERAL SIGNAL

DRAWING TITLE
ASSEMBLY
STATUS & CONTROL BOARD

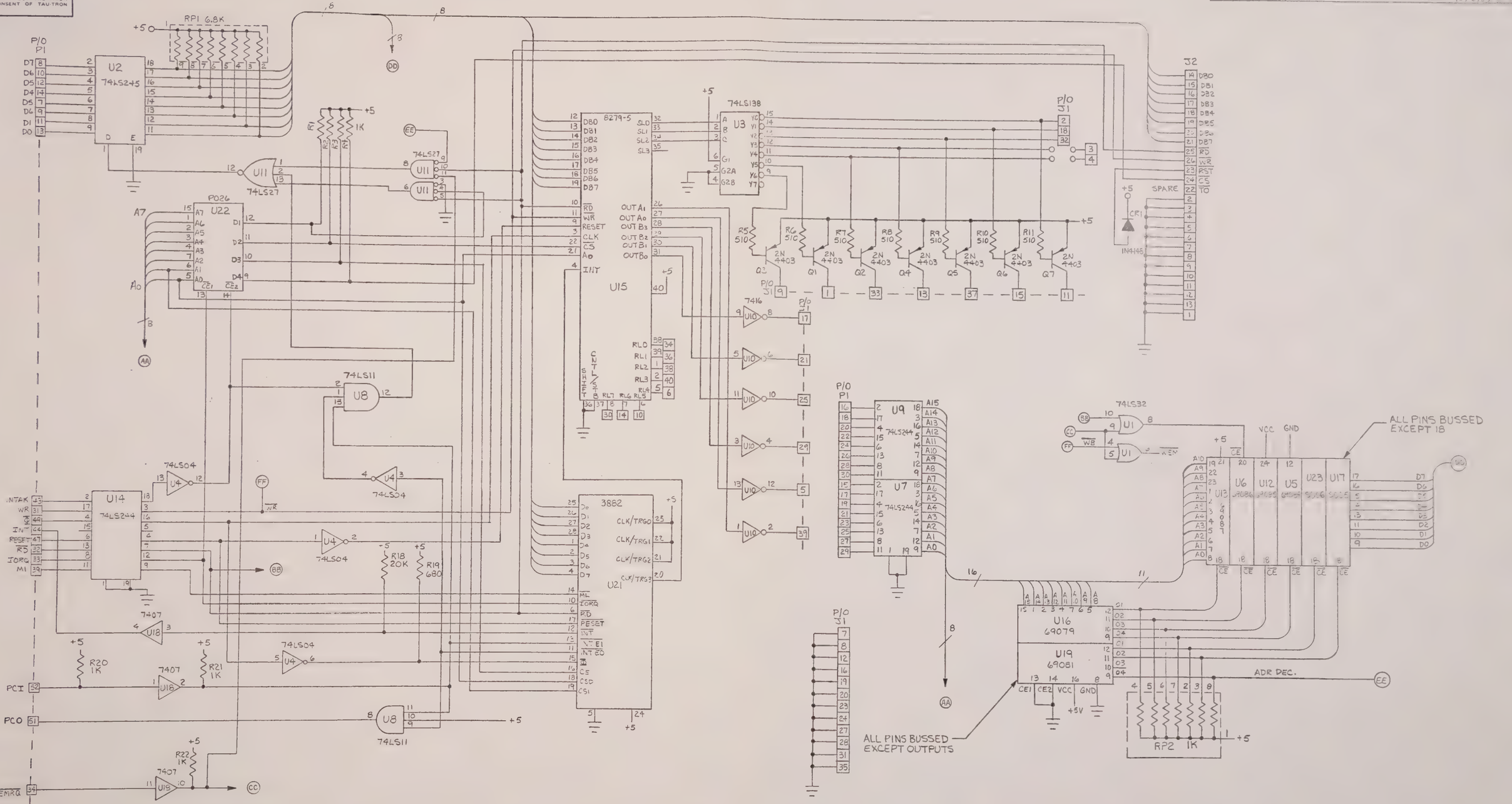
SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57333	E1

SCALE 2:1 | S5200E RX | SHEET 1 OF 1

NOTICE OF CONFIDENTIAL INFORMATION

INFORMATION CONTAINED HEREIN IS CONFIDENTIAL, IS THE PROPERTY OF TAU-TRON INC., IS TO BE USED SOLELY FOR THE PURPOSE PROVIDED, AND IS NOT TO BE DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN CONSENT OF TAU-TRON INC.

REVISIONS			
ZONE	LTR	DESCRIPTION	DATE / APPROVED
	A	REV PER ECO# 02125 TMM 4-3-85	5/6/85



LAST REF DES.	
USED	UNUSED
C12	C2,C3
CR1	
J2	
Q7	
R22	R12-R17
RP2	
U23	U20

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

57372	S5200E RX
NEXT ASSY	USED ON

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ½
FRACTIONS ± ½
3 PLACE DECIMALS ± ½
2 PLACE DECIMALS ± ½
1 PLACE DECIMALS ± ½
MATERIAL: ½

CONTR NO.

DR	T. MURPHY	4-3-85
CHK	[illegible]	5/7/85
APPD		

APPROVED

BY DIRECTION OF

tau-tron A UNIT OF GENERAL SIGNAL

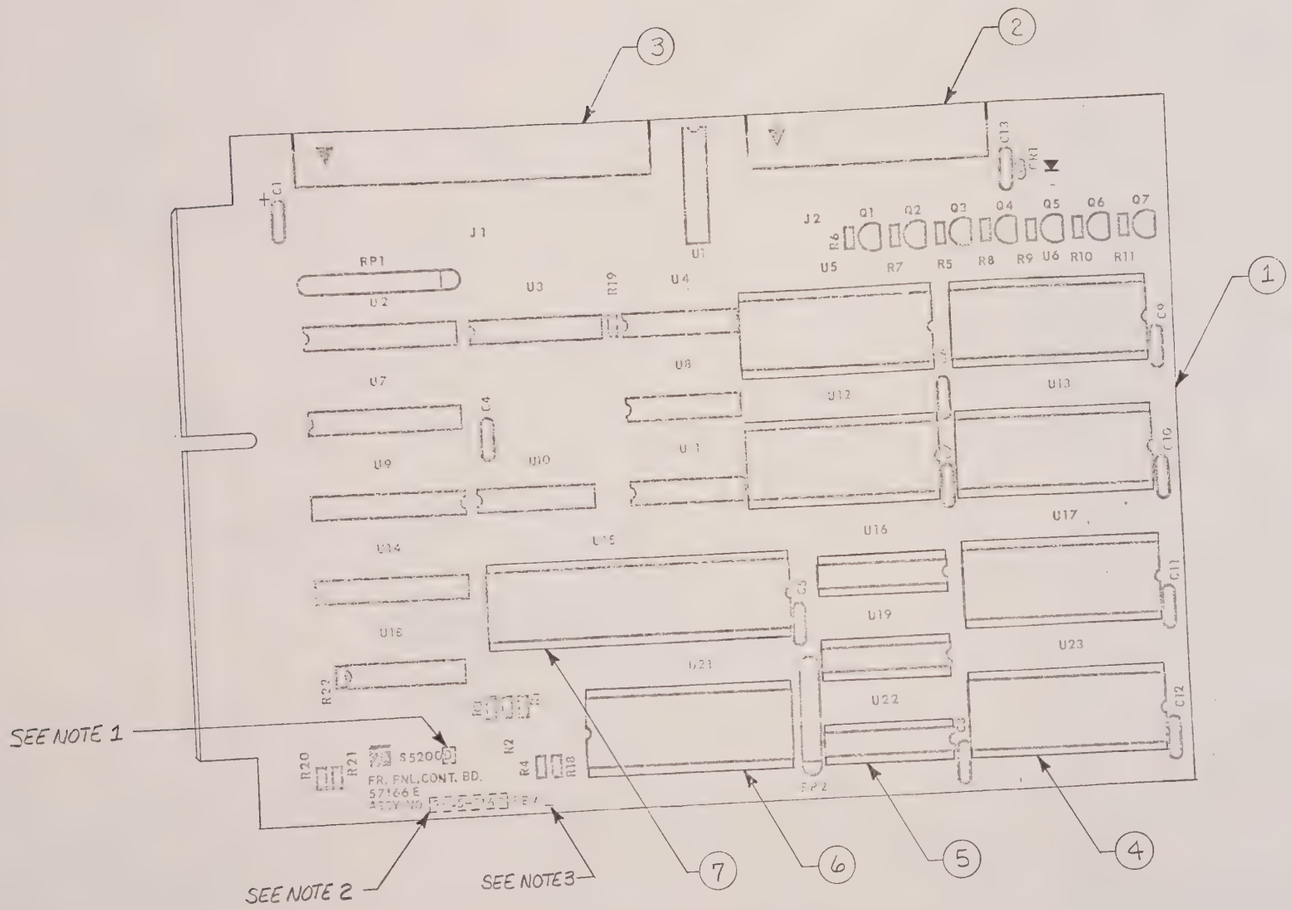
DRAWING TITLE
SCHEMATIC
FRONT PANEL CONTROL BD.

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57371	A

SCALE ½ | S5200E | SHEET 1 OF 1

TARY NOTICE:
DOCUMENT CONTAINS PROPRIETARY INFORMATION
TRON, INC. AND NEITHER THE DOCUMENT NOR SAID
TARY INFORMATION SHALL BE PUBLISHED, REPRO-
COPIED OR DISCLOSED WITHOUT THE EXPRESS WRIT-
MISSION OF A DULY AUTHORIZED REPRESENTATIVE
COMPANY.

-	A	REV PER ECO



NOTES:
1. REMOVE 'D' AND REPLACE IT WITH "E".
2. REMOVE 9545-7167 AND REPLACE IT WITH 9545-7372.
3. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
FRACTIONS ± 1/64
2 PLACE DECIMALS ± .01
1 PLACE DECIMALS ± .1
MATERIAL

DRN Martha Spindler 85MAR26
CHK
APD 3/29/85

tau-tron
DRAWING TITLE
ASSEMBLY
FRONT PANEL CONTROL PCB

57061 S5200E RX
NEXT ASSY USED ON
APPLICATION

954 57372 REV A
2/1 S5200E 1 OF 1

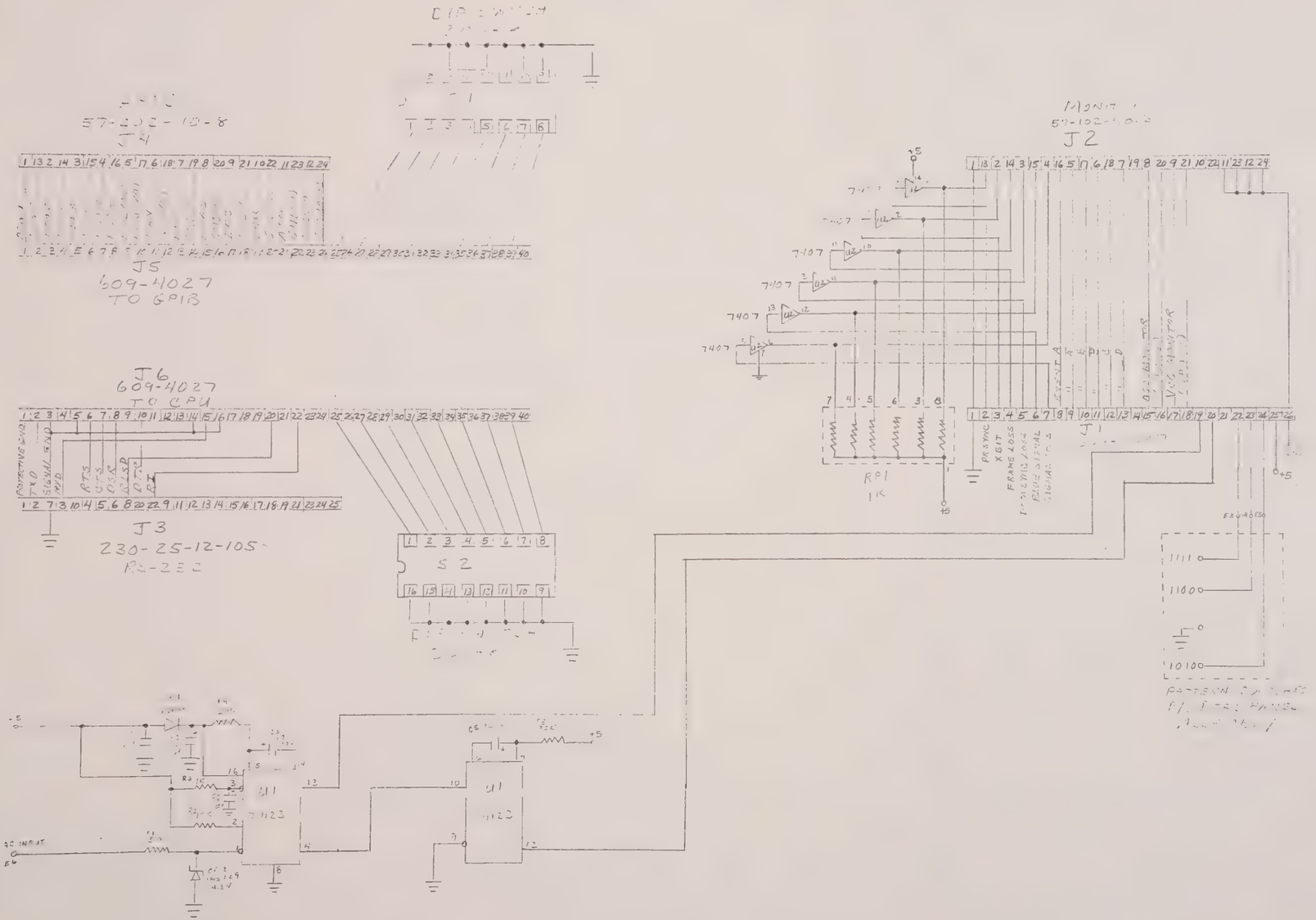
57372

PROPRIETARY NOTICE:

THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

REVISIONS

ZONE	LTR	DESCRIPTION		
	A	RELEASED TO MFG. PER ECO 01056	7-8-82	DT
	B	REV PER ECO-01120 REVISED 4-19-82	4-26-82	[illegible]
	C	UPDATED PER DOC ACCURACY PROG PER ECO 02194 8-5-85 SH	8/27/85	PK



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTOR VALUES ARE IN OHMS, 1/4W, ± 5%.
   B. CAPACITOR VALUES ARE IN MICROFARADS.

REFERENCE DESIGNATIONS	
LAST USED	LAST USED
C5	U2
CR2	
J6	
R5	
RP1	
S2	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

C 57274	55200D RX
NEXT ASSY	USED ON

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± 4
FRACTIONS ± 4
3 PLACE DECIMALS ± 4
2 PLACE DECIMALS ± 4
1 PLACE DECIMALS ± 4
MATERIAL: 4

CONTR NO.

DR	K. Graupre	
CHK	RL	11-4-81
APPD	K. Pearce	11-4-81

APPROVED

BY DIRECTION OF

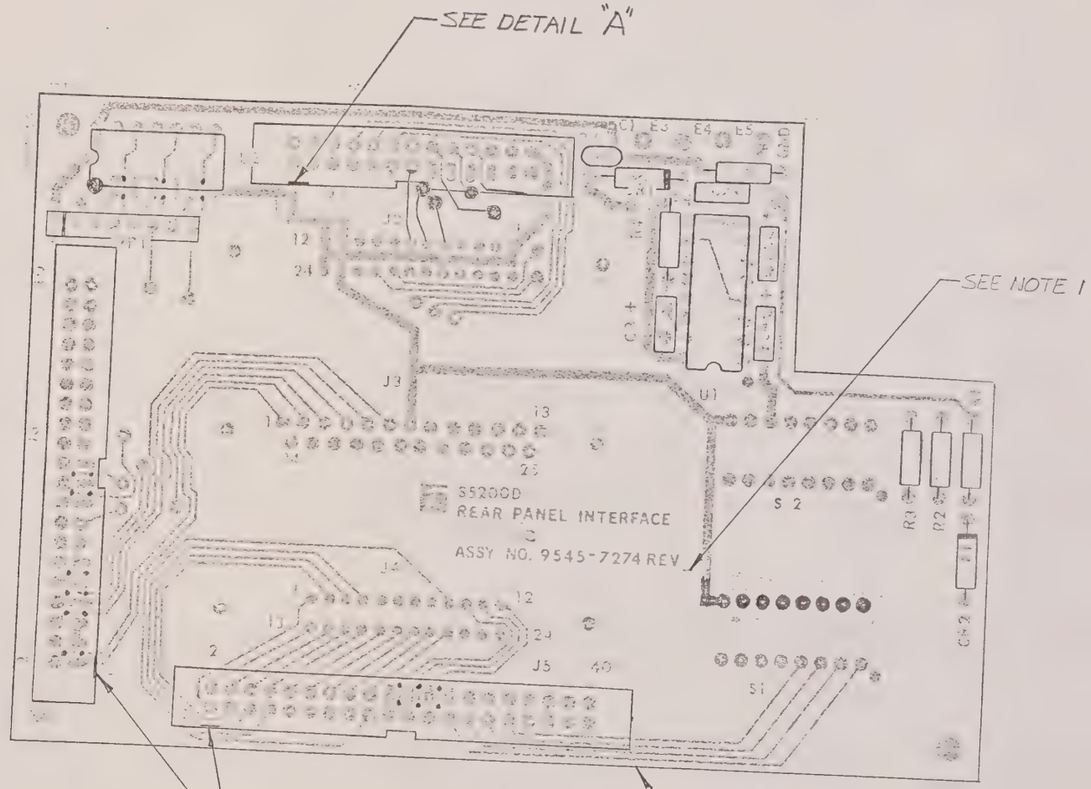
tau-tron inc
Chelmsford, Ma. 01824

DRAWING TITLE
SCHEMATIC
REAR PANEL INTERFACE P.C.B.

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	4	57272	C

SCALE 4 | 55200D RX | SHEET 1 OF 1

	F	UPDATED PER DOC ACCURACY PROG PER ECO #02194 7-18-85		
	G	REV PER ECO #02949 ETR 10/30/87	EE/Jan6	



DETAIL "A"

NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. ASSEMBLE STANDOFFS TO CONNECTOR J3 PRIOR TO SOLDERING TO BOARD. USE HARDWARE SUPPLIED WITH STANDOFF.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	
	S5200D RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL: ~

CONTR NO.

DR	C.S.	11-3-81
CHK	R.L.	11-4-81
APPD	K.B.	11-4-81

APPROVED
BY

tau-tron INSTRUMENTS GENERAL PROD.

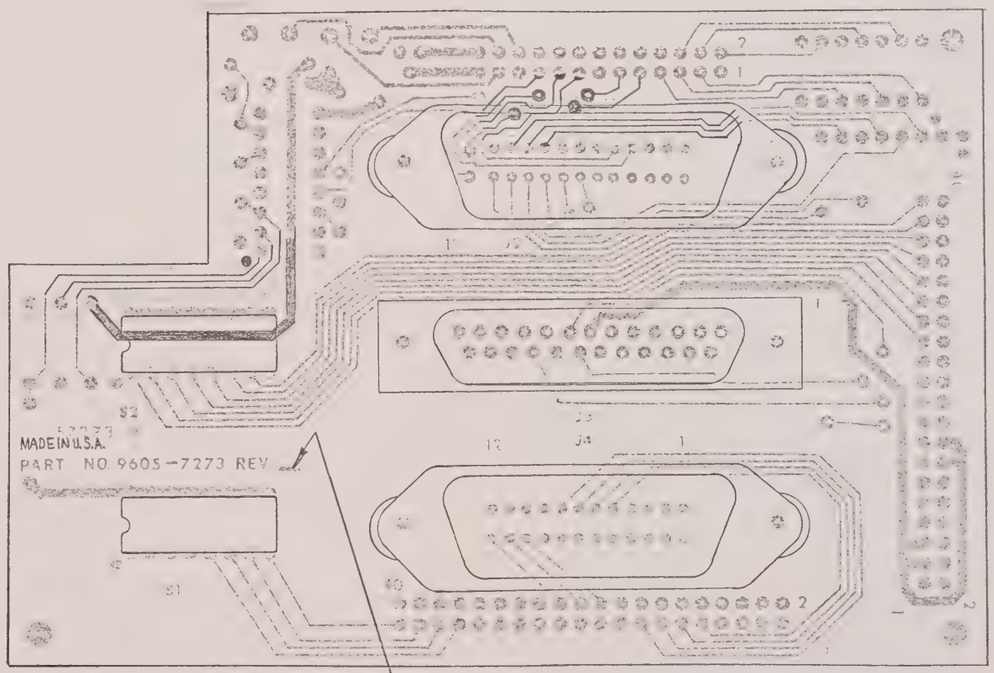
DRAWING TITLE
ASSEMBLY - REAR PANEL INTERFACE PCB (GPIB OPT)

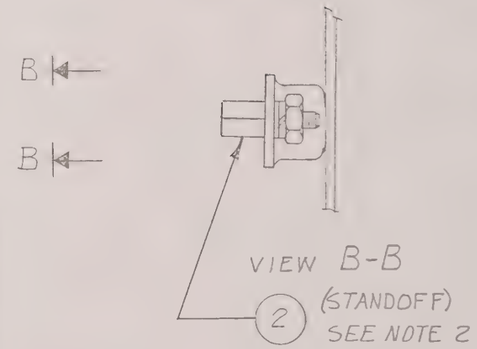
SIZE	DRAWING NO.	REV
C	954 57274	G

SCALE 2/1 S5200D RX SHEET 1 OF 2

57274

	G	SEE SHEET 1 FOR REVISIONS





	S5200D RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
1 PLACE DECIMALS ±
2 PLACE DECIMALS ±
3 PLACE DECIMALS ±
MATERIAL

CONTR NO.		
DR	C. S.	11-3-81
CHK	R.L.	11-4-81
APPR	K.B.	11-4-81
APPROVED		

tau-tron

DRAWING TITLE
ASSEMBLY - REAR PANEL
INTERFACE PCB (GPIB OPT)

SIZE	DRAWING NO.	REV
C	954 57274	G

SCALE 2/1 S5200D RX SHEET 2 OF 2

PROPRIETARY NOTICE
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION
STATRON, INC. AND NEITHER THIS DOCUMENT NOR SAID
PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRO-
DUCED OR DISCLOSED WITHOUT THE EXPRESS WRIT-
TEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE
OF SAID COMPANY

REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APD
	A	REV PER ECO# 02125	5/1/13	

COMPONENT SIDE

J1 FRONT PANEL CONTROL

	+5V	2	1	+5V
	GND	4	3	GND
	-5.2V	6	5	-5.2V
(J1-8)	D7	8	7	D3 (J1-7)
(J1-9)	D6	10	9	D2 (J1-6)
(J1-10)	D5	12	11	D1 (J1-5)
(J1-11)	D4	14	13	D0 (J1-4)
(J1-12)	A15	16	15	A7 (J1-3)
(J1-13)	A14	18	17	A6 (J1-2)
(J1-14)	A13	20	19	A5 (J1-1)
(J1-15)	A12	22	21	A4 (J1-24)
(J1-16)	A11	24	23	A3 (J1-23)
(J1-17)	A10	26	25	A2 (J1-22)
(J1-18)	A9	28	27	A1 (J1-21)
(J1-19)	A8	30	29	A0 (J1-20)
(J1-20)	RD	32	31	WR (J1-19)
(J1-21)	WAIT/REQ	34	33	IORQ (J1-18)
(J1-22)	MEMRD	36	35	MEMWR (J1-17)
(J1-23)	M1/SYNC	38	37	RESET (J1-16)
(J1-24)	INT/BUSAK	40	39	STATUS (J1-15)
(J1-25)	BUSRQ	42	41	BUSAK (J1-14)
(J1-26)	WAIT/REQ	44	43	INTAK (J1-13)
(J1-27)	IOWR/RD	46	45	WAIT/RQ (J1-12)
(J1-28)	RESET	48	47	SYSRESET (J1-11)
(J2-50)	CNTRL	50	49	CLOCK (J2-49)
(J1-51)	PCI	52	51	PCO
(J1-53)	AUX GND	54	53	AUX +5
(J2-56)		56	55	(J2-55)

J2 CPU (SEE NOTE 4)

	+5V	2	1	+5V
	GND	4	3	GND
	-5.2V	6	5	-5.2V
(J1-8)	D7	8	7	D3 (J1-7)
(J1-9)	D6	10	9	D2 (J1-6)
(J1-10)	D5	12	11	D1 (J1-5)
(J1-11)	D4	14	13	D0 (J1-4)
(J1-12)	A15	16	15	A7 (J1-3)
(J1-13)	A14	18	17	A6 (J1-2)
(J1-14)	A13	20	19	A5 (J1-1)
(J1-15)	A12	22	21	A4 (J1-24)
(J1-16)	A11	24	23	A3 (J1-23)
(J1-17)	A10	26	25	A2 (J1-22)
(J1-18)	A9	28	27	A1 (J1-21)
(J1-19)	A8	30	29	A0 (J1-20)
(J1-20)	RD	32	31	WR (J1-19)
(J1-21)	WAIT/REQ	34	33	IORQ (J1-18)
(J1-22)	MEMRD	36	35	MEMWR (J1-17)
(J1-23)	M1/SYNC	38	37	RESET (J1-16)
(J1-24)	INT/BUSAK	40	39	STATUS (J1-15)
(J1-25)	BUSRQ	42	41	BUSAK (J1-14)
(J1-26)	WAIT/REQ	44	43	INTAK (J1-13)
(J1-27)	IOWR/RD	46	45	WAIT/RQ (J1-12)
(J1-28)	RESET	48	47	SYSRESET (J1-11)
(J2-50)	CNTRL	50	49	CLOCK (J2-49)
(J1-51)	PCI	52	51	PCO
(J1-53)	AUX GND	54	53	AUX +5
(J3-56)		56	55	(J3-55)

J3 CMPIS (SEE NOTE 5)

	+5V	2	1	+5V
	GND	4	3	GND
	-5.2V	6	5	-5.2V
(J4-8)	D7	8	7	D3 (J4-7)
(J4-9)	D6	10	9	D2 (J4-6)
(J4-10)	D5	12	11	D1 (J4-5)
(J4-11)	D4	14	13	D0 (J4-4)
(J4-12)	A15	16	15	A7 (J4-3)
(J4-13)	A14	18	17	A6 (J4-2)
(J4-14)	A13	20	19	A5 (J4-1)
(J4-15)	A12	22	21	A4 (J4-24)
(J4-16)	A11	24	23	A3 (J4-23)
(J4-17)	A10	26	25	A2 (J4-22)
(J4-18)	A9	28	27	A1 (J4-21)
(J4-19)	A8	30	29	A0 (J4-20)
(J4-20)	RD	32	31	WR (J4-19)
(J4-21)	WAIT/REQ	34	33	IORQ (J4-18)
(J4-22)	MEMRD	36	35	MEMWR (J4-17)
(J4-23)	M1/SYNC	38	37	RESET (J4-16)
(J4-24)	INT/BUSAK	40	39	STATUS (J4-15)
(J4-25)	BUSRQ	42	41	BUSAK (J4-14)
(J4-26)	WAIT/REQ	44	43	INTAK (J4-13)
(J4-27)	IOWR/RD	46	45	WAIT/RQ (J4-12)
(J4-28)	RESET	48	47	SYSRESET (J4-11)
(J2-50)	CNTRL	50	49	CLOCK (J2-49)
(J4-51)	PCI	52	51	PCO
(J2-53)	AUX GND	54	53	AUX +5
(J3-56)		56	55	(J3-55)

J4 STATUS & CONTROL TT0626

	+5V	2	1	+5V
	GND	4	3	GND
	-5.2V	6	5	-5.2V
(J3-8)	D7	8	7	D3 (J3-7)
(J3-9)	D6	10	9	D2 (J3-6)
(J3-10)	D5	12	11	D1 (J3-5)
(J3-11)	D4	14	13	D0 (J3-4)
(J3-12)	A15	16	15	A7 (J3-3)
(J3-13)	A14	18	17	A6 (J3-2)
(J3-14)	A13	20	19	A5 (J3-1)
(J3-15)	A12	22	21	A4 (J3-24)
(J3-16)	A11	24	23	A3 (J3-23)
(J3-17)	A10	26	25	A2 (J3-22)
(J3-18)	A9	28	27	A1 (J3-21)
(J3-19)	A8	30	29	A0 (J3-20)
(J3-20)	RD	32	31	WR (J3-19)
(J3-21)	WAIT/REQ	34	33	IORQ (J3-18)
(J3-22)	MEMRD	36	35	MEMWR (J3-17)
(J3-23)	M1/SYNC	38	37	RESET (J3-16)
(J3-24)	INT/BUSAK	40	39	STATUS (J3-15)
(J3-25)	BUSRQ	42	41	BUSAK (J3-14)
(J3-26)	WAIT/REQ	44	43	INTAK (J3-13)
(J3-27)	IOWR/RD	46	45	WAIT/RQ (J3-12)
(J3-28)	RESET	48	47	SYSRESET (J3-11)
(J2-50)	CNTRL	50	49	CLOCK (J2-49)
(J3-51)	PCI	52	51	PCO
(J2-53)	AUX GND	54	53	AUX +5
(J3-56)		56	55	(J3-55)

J5 ERROR COUNTER TT0600

	+5V	2	1	+5V
	GND	4	3	GND
	-5.2V	6	5	-5.2V
(J4-8)	D7	8	7	D3 (J4-7)
(J4-9)	D6	10	9	D2 (J4-6)
(J4-10)	D5	12	11	D1 (J4-5)
(J4-11)	D4	14	13	D0 (J4-4)
(J4-12)	A15	16	15	A7 (J4-3)
(J4-13)	A14	18	17	A6 (J4-2)
(J4-14)	A13	20	19	A5 (J4-1)
(J4-15)	A12	22	21	A4 (J4-24)
(J4-16)	A11	24	23	A3 (J4-23)
(J4-17)	A10	26	25	A2 (J4-22)
(J4-18)	A9	28	27	A1 (J4-21)
(J4-19)	A8	30	29	A0 (J4-20)
(J4-20)	RD	32	31	WR (J4-19)
(J4-21)	WAIT/REQ	34	33	IORQ (J4-18)
(J4-22)	MEMRD	36	35	MEMWR (J4-17)
(J4-23)	M1/SYNC	38	37	RESET (J4-16)
(J4-24)	INT/BUSAK	40	39	STATUS (J4-15)
(J4-25)	BUSRQ	42	41	BUSAK (J4-14)
(J4-26)	WAIT/REQ	44	43	INTAK (J4-13)
(J4-27)	IOWR/RD	46	45	WAIT/RQ (J4-12)
(J4-28)	RESET	48	47	SYSRESET (J4-11)
(J2-50)	CNTRL	50	49	CLOCK (J2-49)
(J4-51)	PCI	52	51	PCO
(J2-53)	AUX GND	54	53	AUX +5
(J4-56)		56	55	(J4-55)

J11

	SPARE			
	+5V	2	1	+5V
	GND	4	3	GND
	-5.2V	6	5	-5.2V
(J2-8)	D7	8	7	D2 (J2-7)
(J2-9)	D6	10	9	D2 (J2-6)
(J2-10)	D5	12	11	D1 (J2-5)
(J2-11)	D4	14	13	D0 (J2-4)
(J2-12)	A15	16	15	A7 (J2-3)
(J2-13)	A14	18	17	A6 (J2-2)
(J2-14)	A13	20	19	A5 (J2-1)
(J2-15)	A12	22	21	A4 (J2-24)
(J2-16)	A11	24	23	A3 (J2-23)
(J2-17)	A10	26	25	A2 (J2-22)
(J2-18)	A9	28	27	A1 (J2-21)
(J2-19)	A8	30	29	A0 (J2-20)
(J2-20)	RD	32	31	WR (J2-19)
(J2-21)	WAIT/REQ	34	33	IORQ (J2-18)
(J2-22)	MEMRD	36	35	MEMWR (J2-17)
(J2-23)	M1/SYNC	38	37	RESET (J2-16)
(J2-24)	INT/BUSAK	40	39	STATUS (J2-15)
(J2-25)	BUSRQ	42	41	BUSAK (J2-14)
(J2-26)	WAIT/REQ	44	43	INTAK (J2-13)
(J2-27)	IOWR/RD	46	45	WAIT/RQ (J2-12)
(J2-28)	RESET	48	47	SYSRESET (J2-11)
(J2-50)	CNTRL	50	49	CLOCK (J2-49)
(J11-51)	PCI	52	51	PCO
(J11-53)	AUX GND	54	53	AUX +5
(J12-56)		56	55	(J2-55)

J12

	SPARE			
	+5V	2	1	+5V
	GND	4	3	GND
	-5.2V	6	5	-5.2V
(J3-8)	D7	8	7	D3 (J3-7)
(J3-9)	D6	10	9	D2 (J3-6)
(J3-10)	D5	12	11	D1 (J3-5)
(J3-11)	D4	14	13	D0 (J3-4)
(J3-12)	A15	16	15	A7 (J3-3)
(J3-13)	A14	18	17	A6 (J3-2)
(J3-14)	A13	20	19	A5 (J3-1)
(J3-15)	A12	22	21	A4 (J3-24)
(J3-16)	A11	24	23	A3 (J3-23)
(J3-17)	A10	26	25	A2 (J3-22)
(J3-18)	A9	28	27	A1 (J3-21)
(J3-19)	A8	30	29	A0 (J3-20)
(J3-20)	RD	32	31	WR (J3-19)
(J3-21)	WAIT/REQ	34	33	IORQ (J3-18)
(J3-22)	MEMRD	36	35	MEMWR (J3-17)
(J3-23)	M1/SYNC	38	37	RESET (J3-16)
(J3-24)	INT/BUSAK	40	39	STATUS (J3-15)
(J3-25)	BUSRQ	42	41	BUSAK (J3-14)
(J3-26)	WAIT/REQ	44	43	INTAK (J3-13)
(J3-27)	IOWR/RD	46	45	WAIT/RQ (J3-12)
(J3-28)	RESET	48	47	SYSRESET (J3-11)
(J2-50)	CNTRL	50	49	CLOCK (J2-49)
(J12-51)	PCI	52	51	PCO
(J12-53)	AUX GND	54	53	AUX +5
(J3-56)		56	55	(J3-55)

+5V
CR1 IN4148
C5 120pF

E10 +5V
E2 BATTERY
E3 BAT SW OUT
E4 BAT SW IN
E5 GND
E6 SPKR OUT
E11 BATTERY GND
E12 SPEAKER GND

E2	+5V	2	1
E3	BATTERY	4	3
E4	BAT SW OUT	6	5
E5	BAT SW IN	8	7
E10	GND	10	9
E6	SPKR OUT	12	11
		14	13
E11	BATTERY GND	16	15
E12	SPEAKER GND	18	17
	GND	22	21
	SCALE (J6-19)	24	23
	(J7-25)	26	25

26 PIN CONNECTOR

FRONT VIEW

NOTES:
1. Ⓔ INDICATES CONNECTIONS TO FRONT PANEL ENDS.
2. Ⓔ INDICATES CONNECTIONS TO REAR PANEL ENDS.
3. ○ INDICATES HARD SOLDER CONNECTIONS.
4. IF OPTION 03, E2 THRU E, IS NOT INSTALLED, CPU ASSEMBLY 9545-7211, OR CPU ASSEMBLY 9545-7251, IS INSTALLED IN J2.
5. IF OPTION 02, CMPIS, NOT INSTALLED, J3 IS LEFT EMPTY.

LAST REF DES USED

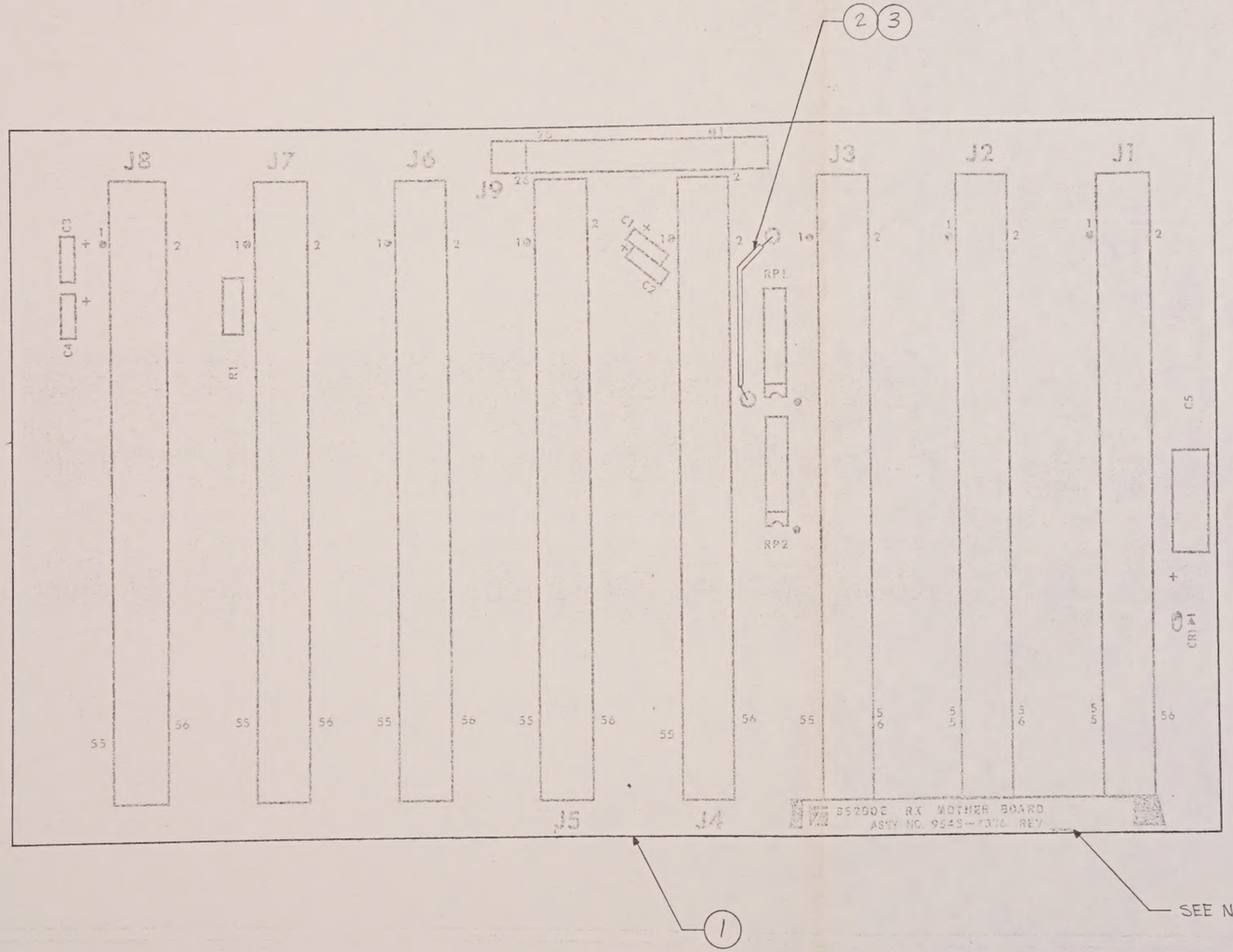
R1	C5	RP2	
J12	E29	CR1	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ± MATERIAL	CONTR NO	tau-tron INC. Chatsworth, CA 91311
		DR BLDD	DRAWING TITLE SCHEMATIC, RECEIVER MOTHER BOARD PC3
		CHK'L KL	
		APPD	
D-57334	5523D E REV	APPROVED	SIZE E CODE IDENT NO CRAWING NO 57334 REV A
NEXT ASSY	USED ON	DIRECTION OF	
APPLICATION		SCALE 4/3	SHEET 1 OF 1

PROPRIETARY NOTICE:
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF TAU-TRON, INC. AND NEITHER THE DOCUMENT NOR SAID PROPRIETARY INFORMATION SHALL BE PUBLISHED, REPRODUCED, COPIED OR DISCLOSED WITHOUT THE EXPRESS WRITTEN PERMISSION OF A DULY AUTHORIZED REPRESENTATIVE OF SAID COMPANY.

REVISIONS

	A	REV PER ECO# 02125 m.s. 22APR85	5/28/85	PK
	B	UPDATED PER DOC ACCURACY PROG PER ECO# D2233 8-23-85 DH	8/30/85	PK



NOTES:
1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

57336

	T. MURPHY	9-17-84	tau-tron
	BUDD	9-20-84	ASSEMBLY MOTHER BOARD S5200E
	J E Stella	9/20/84	
NEXT ASSY	USED ON		
S5200ERX			
MATERIAL			
4			
D 954		57336	REV B
S5200E			SHT 1 OF 1

10 Lyberty Way, Westford, MA 01886
(617) 692-5100 Telex 750245 1-800-TAU-TRON (Outside MA)